

Presentation of TC-8

Assistants 2009

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- 1 Overview of the tarball
- 2 Graph
- 3 Flowgraph
- 4 Liveness
- 5 Interference
- 6 Testing

Overview of the tarball

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The tree structure of TC-8

- New directory:
 - 'src/liveness': Where you will work. Graphs describing the flow of control, the liveness and the interferences.
- New files:
 - 'src/liveness/interp.c': The main part of the interpreter.

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- New files:
 - 'lib/rtlsc/graphs': Generic implementation of graph.

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Quick overview of Graph

- Rely on boost.
- Implemented as an adjacency graph.
- Take a look at methods and free functions for the class `adjacency_list`.

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Flowgraph

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- Nodes of the graph are instructions.
- If an instruction a can be followed by another b , then an edge is created between a and b .
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Quick overview of Liveness

- Answer to the question: who is alive just before the instruction, and who just afterwards?
- Rely on an iterative algorithm with a fixed point (even the least fixed point!).

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Interference

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Quick overview of Interference

- Nodes of the graph are temporaries.
- If temporary a is alive at the same time than b , then they interfere, and an edge is created.
- The Interference graph is used for the register allocation.
- What about MOVE?

```
graph TD
    A((a)) --- B((b))
    B --- C((c))
    C --- D((d))
    D --- E((e))
    E --- F((f))
    F --- G((g))
    G --- H((h))
    H --- I((i))
    I --- J((j))
    J --- K((k))
    K --- L((l))
    L --- M((m))
    M --- N((n))
    N --- O((o))
    O --- P((p))
    P --- Q((q))
    Q --- R((r))
    R --- S((s))
    S --- T((t))
    T --- U((u))
    U --- V((v))
    V --- W((w))
    W --- X((x))
    X --- Y((y))
    Y --- Z((z))
    Z --- AA((a))
```

Example: a and b must be in the same register.
The other variables can be in any register.

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