Mechanizing the Minimization of Deterministic Generalized Büchi Automata

Souheib Baarir^{1,2} <u>Alexandre Duret-Lutz</u>³

¹Université Paris Ouest Nanterre la Défense, Nanterre, France ²Sorbonne Universités, UPMC Univ. Paris 6, UMR 7606, LIP6, Paris, France souheib.baarir@lip6.fr

> ³LRDE, EPITA, Le Kremlin-Bicêtre, France adl@lrde.epita.fr

FORTE'14, 3-5 June 2014

Context



 Büchi Automata are used in many formal methods, but with different requirements.





Context







 Büchi Automata are used in many formal methods, but with different requirements.

Small [D]BA helps

- Minimization (NP-comp.),
- Simulation-based algorithms,
- generalized acceptance,
- transition-based acceptance.

Transion-based Generalized Acceptance

Minimal Büchi automaton for $GFa \wedge GFb$:





Transion-based Generalized Acceptance

Minimal automata for $GFa \wedge GFb$:



Using Transition-based and Generalized acceptance allows more compact automata.

Transion-based Generalized Acceptance

Minimal automata for $GFa \wedge GFb$:



Using Transition-based and Generalized acceptance allows more compact automata.

Objective





Small [D]BA helps

- Minimization (NP-comp.),
- Simulation-based algorithms,
- generalized acceptance,
- transition-based acceptance.
- Our objective: building minimal DTGBA



Objective



Objective



Introduction

2 General Framework

LTL Hierarchy: Determinization & Minimization Our Proposed Framework

3 SAT-based Minimization

Equivalence Check of Two DTGBA SAT-Based Synthesis of Equivalent DTGBA Minimization by Iterative Synthesis

4 Conclusion







 Recurrence properties are DBA-realizable. (E.g. via Rabin)







- Recurrence properties are DBA-realizable. (E.g. via Rabin)
- WDBA can be minimized in polynomial time.

C. Dax, J. Eisinger, and F. Klaedtke. Mechanizing the powerset construction for restricted classes of ω-automata. ATVA'07



- Recurrence properties are DBA-realizable. (E.g. via Rabin)
- WDBA can be minimized in polynomial time.
- Some recurrences (the TCONG class) can always be determininized to DTBA by powerset construction.





- Recurrence properties are DBA-realizable. (E.g. via Rabin)
- WDBA can be minimized in polynomial time.
- Some recurrences (the TCONG class) can always be determininized to DTBA by powerset construction.
- So far, no technique for:
 - Determinization of TGBA,
 - Minimization of DTGBA.

6/14

C. Dax, J. Eisinger, and F. Klaedtke. Mechanizing the powerset construction for restricted classes of ω-automata. ATVA'07

Output: DBA. (Ehlers' setup.)



R. Ehlers. Minimising DBA precisely using SAT solving. SAT'10

S. C. Krishnan et al. Deterministic ω-automata vis-a-vis DBA. ISAAC'94

7/14











Output: DTGBA (m > 1) or DTBA (m = 1).



Output: DTGBA (m > 1) or DTBA (m = 1). Our setup.



Introduction

2 General Framework

LTL Hierarchy: Determinization & Minimization Our Proposed Framework

3 SAT-based Minimization

Equivalence Check of Two DTGBA SAT-Based Synthesis of Equivalent DTGBA Minimization by Iterative Synthesis

4 Conclusion





















Two **complete** DTGBA \mathcal{A} and \mathcal{B} are equivalent iff: for each elementary cycle c of $\mathcal{A} \otimes \mathcal{B}$, $c_{|\mathcal{A}|}$ is accepting $\iff c_{|\mathcal{B}|}$ is accepting.



Now, given a reference \mathcal{A} , does a smaller equivalent \mathcal{B} exist?



SAT-Based Synthesis of Equivalent DTGBA

We look for an automaton \mathcal{B} equivalent to \mathcal{A} , but with $|\mathcal{A}| - 1$ states and *m* acceptance sets.

- 1 Encode as a SAT problem:
 - Some Boolean variables represent all possible transitions in \mathcal{B} .
 - More Boolean variables represent all possible cycles in the product $\mathcal{A} \otimes \mathcal{B}$.
 - Constraints ensure that transitions in the product are letter-compatible, and the *elementary cycle acceptance* condition is fulfilled.

- 2 Run a SAT solver:
 - If the problem is UNSAT, then a smaller DTGBA does not exist.
 - Otherwise the solution contains an encoding of \mathcal{B} .



SAT-Based Synthesis of Equivalent DTGBA

We look for an automaton \mathcal{B} equivalent to \mathcal{A} , but with $|\mathcal{A}| - 1$ states and *m* acceptance sets.

- 1 Encode as a SAT problem:
 - Some Boolean variables represent all possible transitions in \mathcal{B} .
 - More Boolean variables represent all possible cycles in the product $\mathcal{A} \otimes \mathcal{B}$.
 - Constraints ensure that transitions in the product are letter-compatible, and the *elementary cycle acceptance* condition is fulfilled.

Differs from Ehlers' approach in the support for **generalized acceptance**, and some **SCC-based** encoding **optimizations**.

2 Run a SAT solver:

- If the problem is UNSAT, then a smaller DTGBA does not exist.
- Otherwise the solution contains an encoding of \mathcal{B} .



- MINIMIZE($\mathcal{A}, m = \mathcal{A}.nb_acc()$): repeat:
 - $n \leftarrow \mathcal{A}.nb_states()$ $\mathcal{B} \leftarrow Synthesize(\mathcal{A}, n-1, m)$ if \mathcal{B} does not exists:

return A

 $\mathcal{A} \leftarrow \mathcal{B}$













🗕 DBA 🛥 DTBA 🛥 DTGBA



🗕 DBA 🛥 DTBA 🛥 DTGBA



- We extended Ehlers' approach with:
 - support generalized and transition-based acceptance,
 - SCC-based optimizations of the encoding (not discussed here)
- We integrated this minimization procedure in a more general framework supporting different determinization procedures, and a faster minimization procedure for weak automata.
 - Our tool is integrated in Spot 1.2.3, available from http://spot.lip6.fr/
 - Instructions for building minimal D[T][G]BA are at http://spot.lip6.fr/userdoc/satmin.html
- We ran a large benchmark exploring the effects of this minimization on many DTGBA generated from LTL formulas.



- Comparing the minimal automata computed in our benchmark with automata produced by LTL→TGBA or LTL→BA translators suggests that these tools could be improved in many cases.
- We can create minimal DTGBA with *m* acceptance conditions, but it is not clear how to select the right *m*.
- We believe the technique can easily be extended to deal with Rabin or Streett acceptance.

