

SPIN'25

# Growing an $\omega$ -Automaton Library

Alexandre Duret-Lutz (EPITA, France)

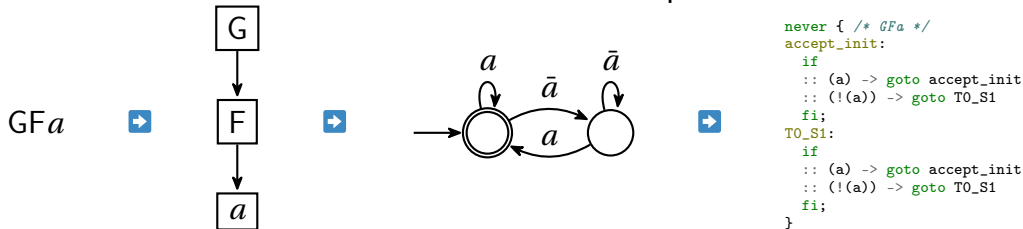
# What is Spot?

*A platform for manipulation of LTL formulas and  $\omega$ -automata. With three interfaces.*

- ▶ C++17 library
- ▶ Command-line tools
- ▶ Python bindings

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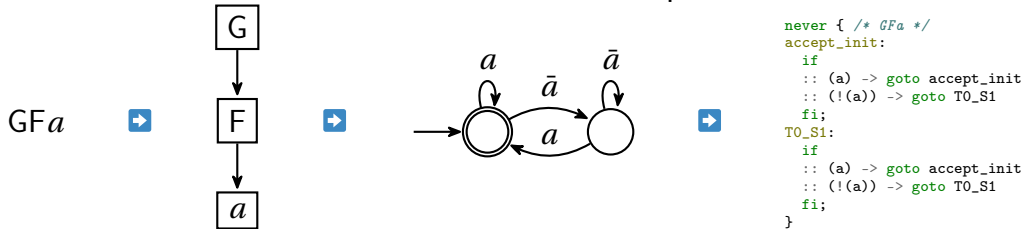
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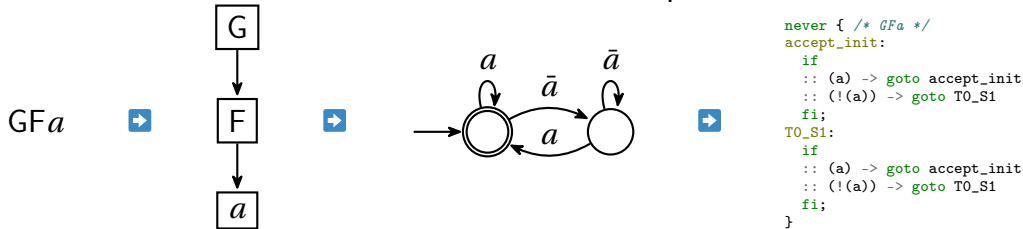
```
spot::parsed_formula pf =
  spot::parse_infix_psl("GFa");
if (pf.format_errors(std::cerr))
  exit(1);
spot::translator trans;
trans.set_type(spot::postprocessor::Buchi);
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## ► Command-line tools

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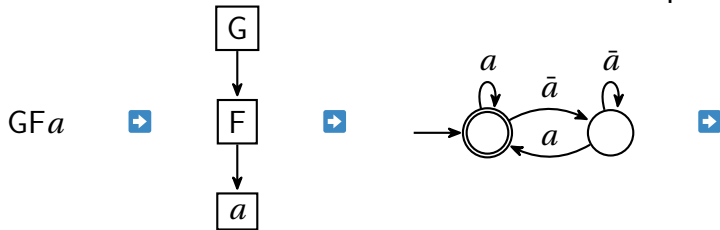
## ► Command-line tools

```
% lt12tgba --spin GFa
```

## ► Python bindings

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```
never { /* GFa */
accept_init:
  if
  :: (a) -> goto accept_init
  :: (!a) -> goto T0_S1
fi;
T0_S1:
  if
  :: (a) -> goto accept_init
  :: (!a) -> goto T0_S1
fi;
}
```

## ► C++17 library

```
spot::parsed_formula pf =
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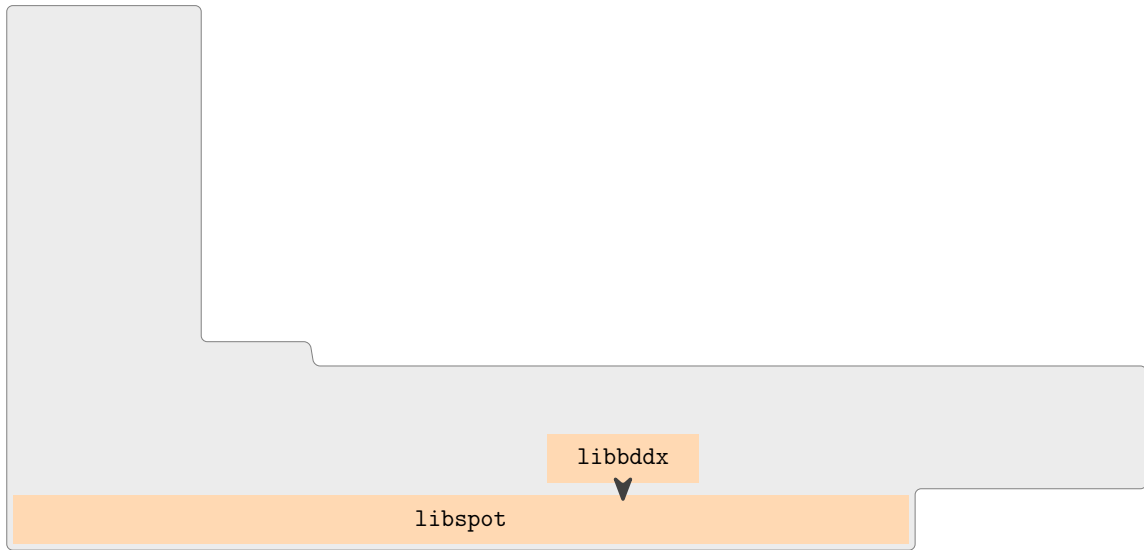
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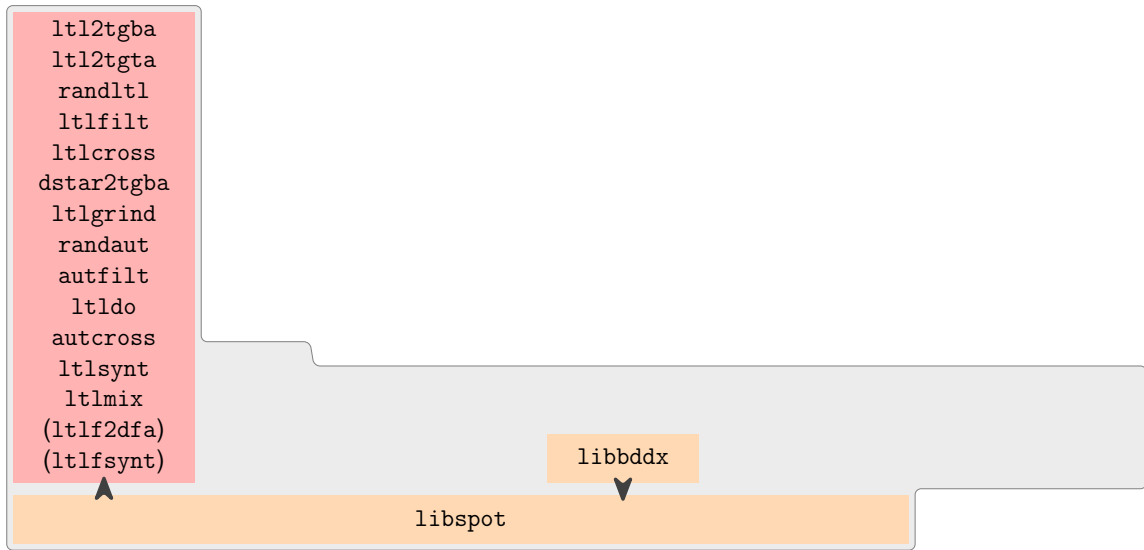
```
import spot
a = spot.translate('GFa', 'buchi', 'sbacc')
print(a.to_str('spin'))
```

## ► With graphical representations in Jupyter

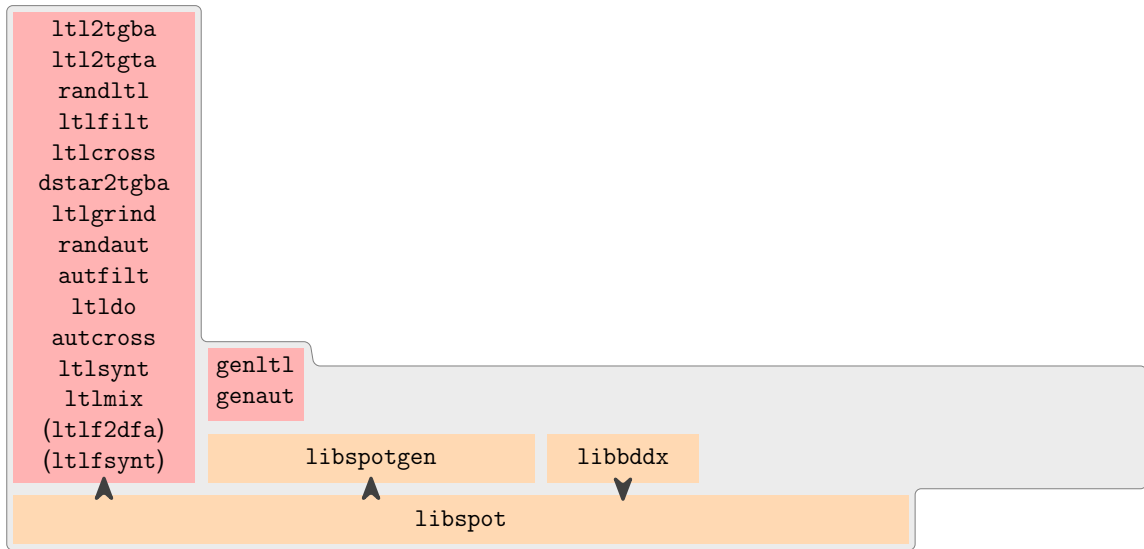
# Architecture



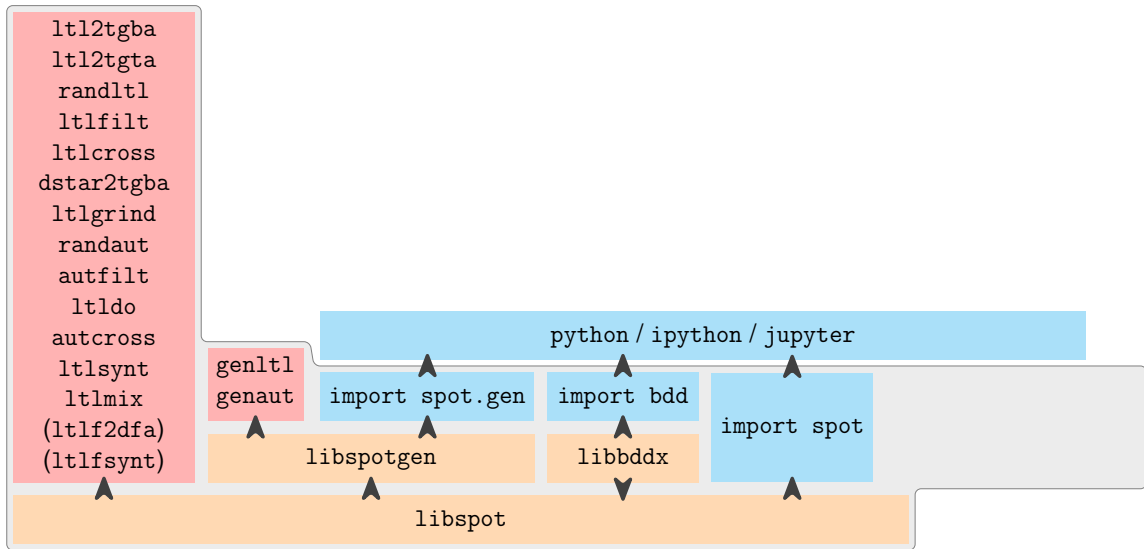
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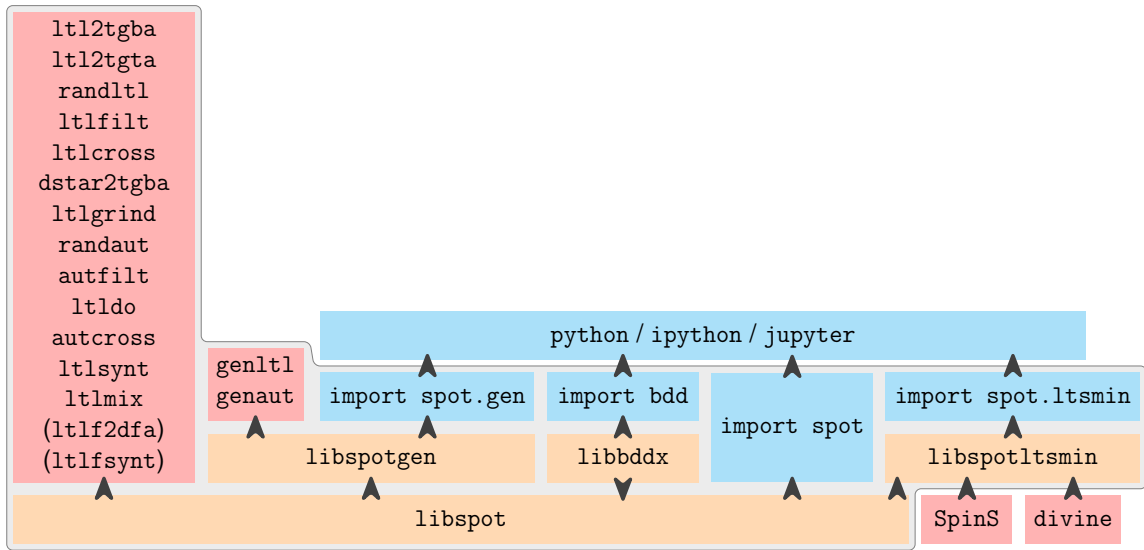
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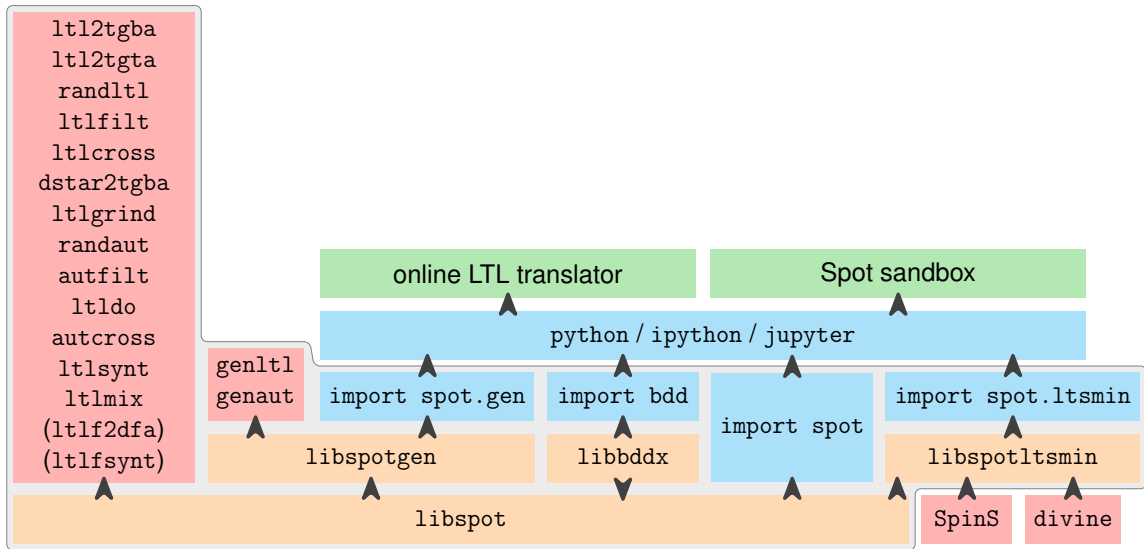
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# The evolution

## 1 Model checking library (2003–2012)

- ▶ using TGBA for model checking
- ▶ pure C++ library
- ▶ no tools (except for the test suite)

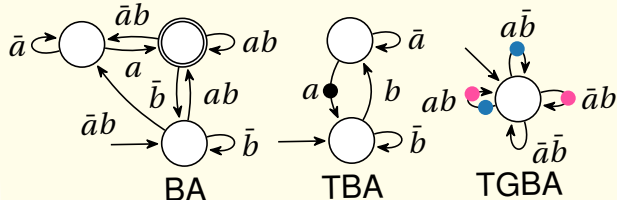


Duret-Lutz and Poitrenaud. *Spot: an extensible model checking library using transition-based generalized Büchi automata*. **MASCOTS'04**. [▶ doi](#)

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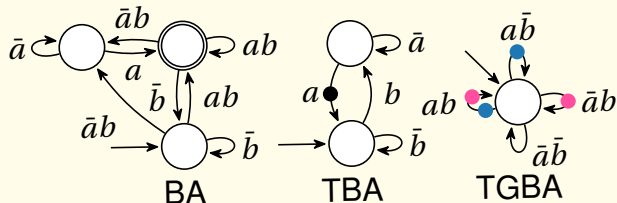


Fig. 7 illustrates how we have connected Spot to GreatSPN<sup>3</sup>. GreatSPN can produce a symbolic reachability graph (SRG) for a Colored Petri net by exploiting its symmetries [19]. We have implemented this interface as a `tgba` subclass whose methods simply delegate their work to the corresponding procedures of GreatSPN. From the point of view of Spot, an SRG appears as any other TGBA.

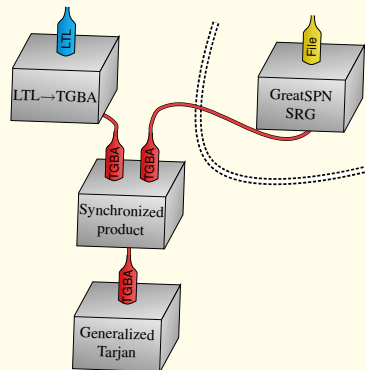


Fig. 7. Interfacing third party state graph generators.

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- ▶ HOA format
- ▶ major rewrite
- ▶ more tools
- ▶ Jupyter support



Duret-Lutz et al. *Spot 2.0 — a framework for LTL and  $\omega$ -automata manipulation*. **ATVA'16**. [▶ doi](#)



Babiak et al. *The Hanoi Omega-Automata format*. **CAV'15**. [▶ doi](#)

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## 4 New application: Synthesis

- ▶ build on existing features
- ▶ improves existing features
- ▶ adds games, mealy machines



# What I will present

## 1 Model checking library (2003–2012)

- ▶ A bug!

## 2 Platform for LTL manip. and model checking (2012–2015)

- ▶ command-line tools
- ▶ SAT-based minimization

## 3 Platform for LTL and $\omega$ -automata (2016–)

- ▶ Jupyter visualizations
- ▶ Emerson-Lei acceptance conditions

## 4 New application: Synthesis

- ▶ `ltlsynt`, ACD
- ▶ `ltlfsynt`, MTDFA

**A memorable bug**  
**(Reported in 2007; fixed in 2009.)**

[Spin'07]

## LTL Satisfiability Checking

Kristin Y. Rozier<sup>1</sup> \* and Moshe Y. Vardi<sup>2</sup>

<sup>1</sup> NASA Langley Research Center, Hampton, Virginia 23681. [Kristin.Y.Rozier@nasa.gov](mailto:Kristin.Y.Rozier@nasa.gov)

<sup>2</sup> Rice University, Houston, Texas 77005, [vardi@cs.rice.edu](mailto:vardi@cs.rice.edu)

**Abstract.** We report here on an experimental investigation of LTL satisfiability checking via a reduction to model checking. By using large LTL formulas, we offer challenging model-checking benchmarks to both explicit and symbolic model checkers. For symbolic model checking, we use both CadenceSMV and NuSMV. For explicit model checking, we use SPIN as the search engine, and we test essentially all publicly available LTL translation tools. Our experiments result in two major findings. First, most LTL translation tools are research prototypes and cannot be considered industrial quality tools. Second, when it comes to LTL satisfiability checking, the symbolic approach is clearly superior to the explicit approach.

# Kristin Rozier's paper

[Spin'07]

## LTL Satisfiability Checking

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information on actual tool performance. We saw that most LTL translation tools, with the exception of SPOT, are research prototypes which cannot be considered industrial quality tools. The focus in the literature has been on minimizing automata size, rather

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## LTL Satisfiability Checking

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**Abstract.** We report here on our experience with LTL satisfiability checking via a reduction to SAT. We offer challenging model-checking benchmarks to model checkers. For symbolic model checking, we use NuSMV. For explicit model checking, we use Spin. We test essentially all publicly available LTL model checkers in two major findings. First, we find that the SAT-based approach and cannot be considered industrial quality. Second, for LTL satisfiability checking, the symbolic approach is clearly superior to the explicit approach.

From: Kristin Yvonne Rozier

Subject: experiments with Spot

Date: 16 Aug 2007

[...]

At SPIN/CAV this year, it was suggested that you might be interested in seeing the few LTL formulas I found which trigger unexpected behavior from Spot. Here is a summary of those formulas.

[...]

# Triggering the bug with “counter formulas”

$C_n$  is a 2-variable LTL formula describing a loop over all values of an  $n$ -bit counter.

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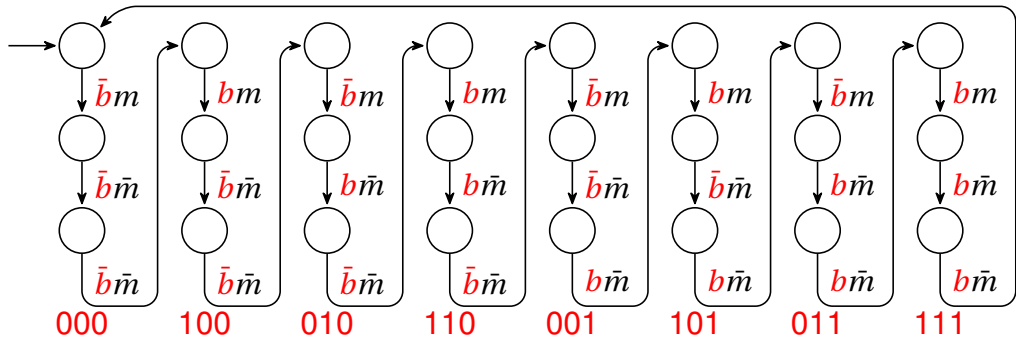
$C_n$  is a 2-variable LTL formula describing a loop over all values of an  $n$ -bit counter.

$$\begin{aligned} C_3 = & \neg b \wedge m \wedge G(m \rightarrow X(\neg m \wedge X(\neg m \wedge X m))) \wedge X(\neg b \wedge X \neg b) \wedge G((\neg b \wedge m) \rightarrow \\ & X(XXb \wedge ((\neg m \wedge (b \leftrightarrow XXXb)) \vee m))) \wedge G((b \wedge m) \rightarrow X(XX\neg b \wedge ((b \wedge \neg m \wedge \\ & XXX\neg b) \vee (m \vee (\neg b \wedge \neg m \wedge X(XXb \wedge ((\neg m \wedge (b \leftrightarrow XXXb)) \vee m))))))) \end{aligned}$$

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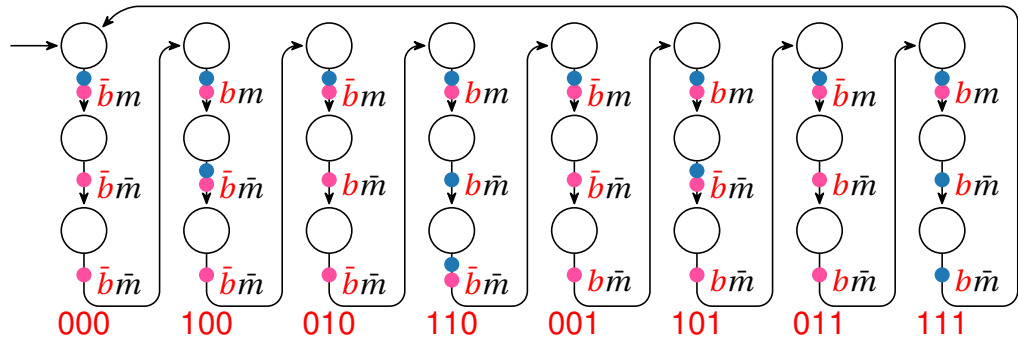


The minimal automaton for  $C_n$  has  $n2^n$  states.

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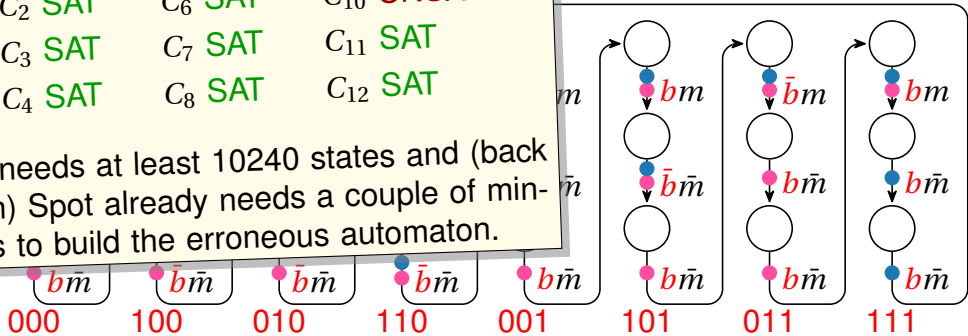
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Kristin's issue with Spot 0.3:

|           |           |                |
|-----------|-----------|----------------|
| $C_1$ SAT | $C_5$ SAT | $C_9$ SAT      |
| $C_2$ SAT | $C_6$ SAT | $C_{10}$ UNSAT |
| $C_3$ SAT | $C_7$ SAT | $C_{11}$ SAT   |
| $C_4$ SAT | $C_8$ SAT | $C_{12}$ SAT   |

$C_{10}$  needs at least 10240 states and (back then) Spot already needs a couple of minutes to build the erroneous automaton.

$n))) \wedge X(\neg b \wedge X\neg b) \wedge G((\neg b \wedge m) \rightarrow$   
 $\wedge G((b \wedge m) \rightarrow X(XX\neg b \wedge ((b \wedge \neg m \wedge$   
 $((\neg m \wedge (b \leftrightarrow XXXb)) \cup m))))))$



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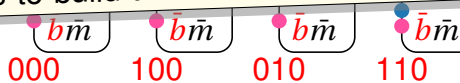
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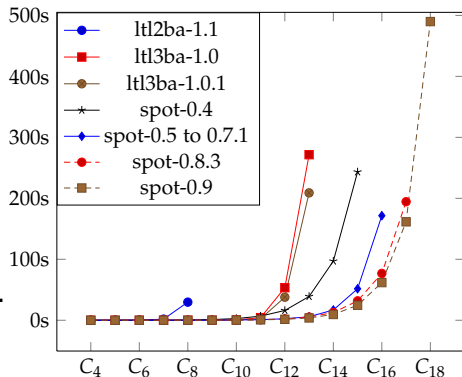
The one-character fix!

```
bool operator<(const formula* left,
               const formula* right)
{
    size_t l = left->hash();
    size_t r = right->hash();
    - if (1 != r)
    + if (l != r)
        return l < r;
    return left->dump() < right->dump();
}
```

The minimal automaton for  $C_n$  has  $n2^n$  states.

# The aftermath

- ▶ I'm now very picky about the font I use in my terminal and editor. (losevka is great! [▶ www](#))
- ▶ Realized that the representation of LTL formulas in Spot 0.4 was really inefficient.
- ▶ Divided translation time by  $\approx 3$  after fixing the bug, by improving formula representations.
- ▶ Today  $C_n$  can be generated by Spot's `genltl` tool.



▶ benchmark from 2012

## Command-line Tools

# Command-line Tools

## Objectives:

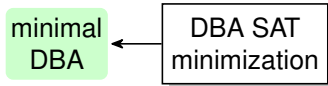
- ▶ Easy access to Spot's algorithms
- ▶ Providing convenient tools for day-to-day experiments
- ▶ Support streaming (a.k.a., pipelining) and scripting (i.e., useful exit codes)
- ▶ Have coherent options among tools
- ▶ Have good defaults
- ▶ Have good error reporting

## **SAT-based minimization of deterministic TGBA**

# From LTL to Minimal D[T][G]BA

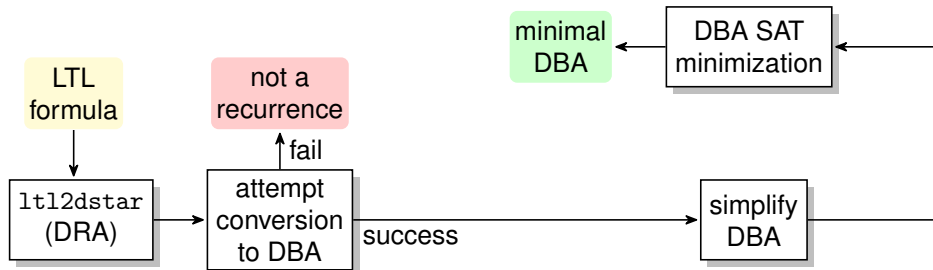
Output: DBA. (Ehlers' setup.)

LTL  
formula



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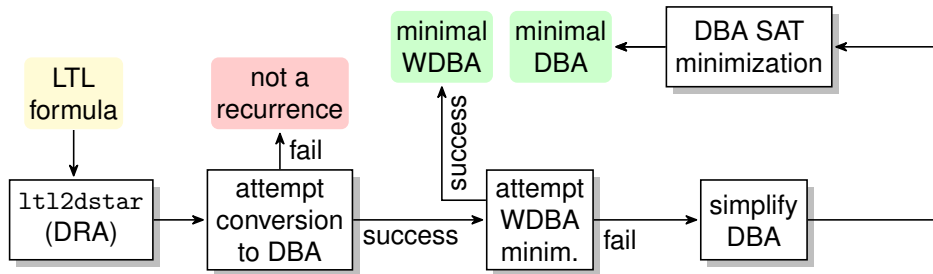


 Klein and Baier. *On-the-fly stuttering in the construction of determ.  $\omega$ -automata*. **CIAA'07**. [doi](#)

 Krishnan, Puri, and Brayton. *Determ.  $\omega$ -automata vis-a-vis determ. Büchi automata*. **ISAAC'94**.

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Output: DBA.

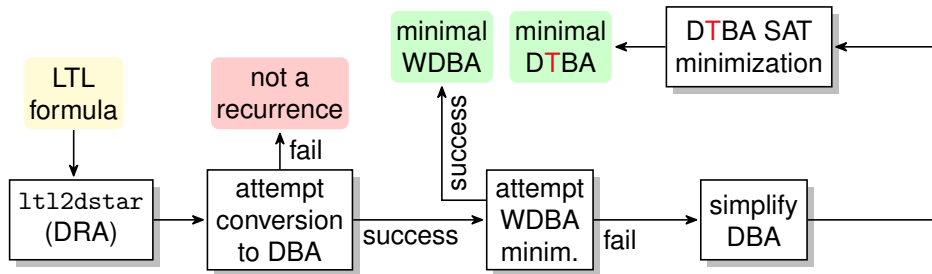


Dax, Eisinger, and Klaedtke. *Mechanizing the powerset construction for restricted classes of*

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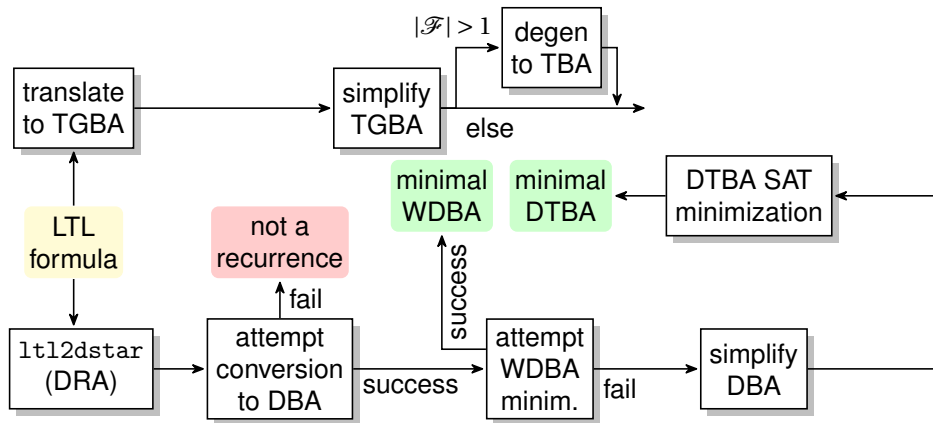
# From LTL to Minimal D[T][G]BA

Output: DTBA.



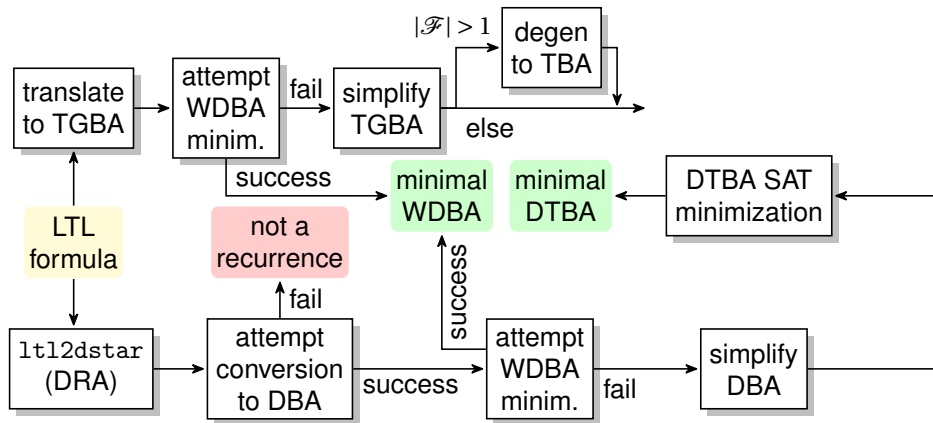
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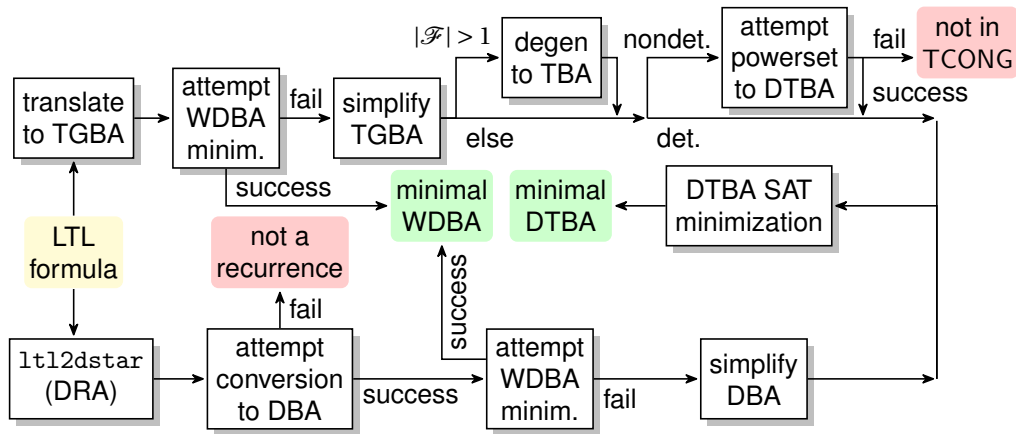
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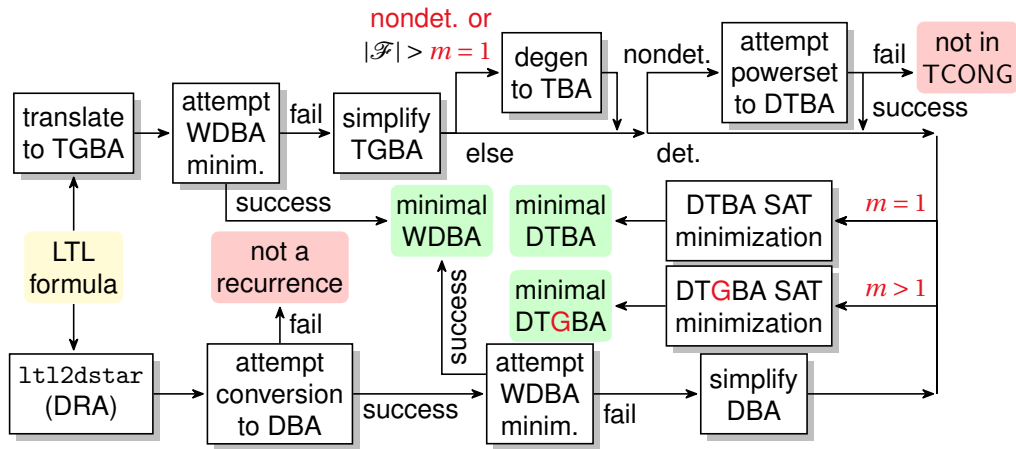


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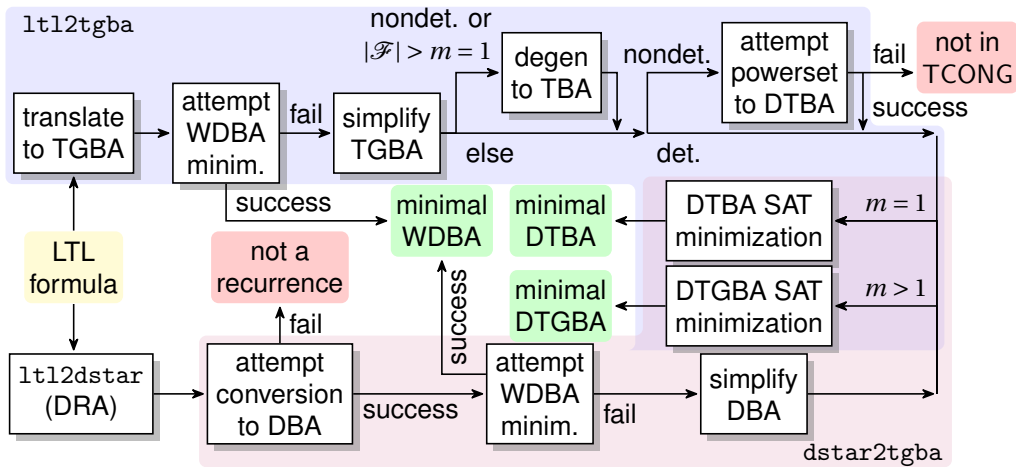
# From LTL to Minimal D[T][G]BA

Output: DTGBA ( $m > 1$ ) or DTBA ( $m = 1$ ).



# From LTL to Minimal D[T][G]BA

Output: DTGBA ( $m > 1$ ) or DTBA ( $m = 1$ ). Our setup.



**Spot 2**  
**HOA + Jupyter  $\Rightarrow$  major rewrite**

# The Hanoi Omega-Automata Format



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## Tool support at publication

ltl2dstar 0.5.3

PRISM 4.3

ltl3ba 1.1.2

Rabinizer 3.1

ltl3dra 0.2.2

Spot 1.99.2

jhoafparser

cpphoafparser



# The Hanoi Omega-Automata Format

## Original motivations

- ▶ Unify output formats for different tools/acceptance conditions
- ▶ Allow **new** acceptance conditions

positive Boolean formulas of  
 $\text{Inf}(x)$  and  $\text{Fin}(y)$  terms,  
a.k.a. Emerson-Lei acceptance

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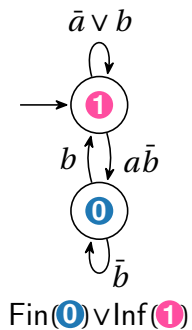
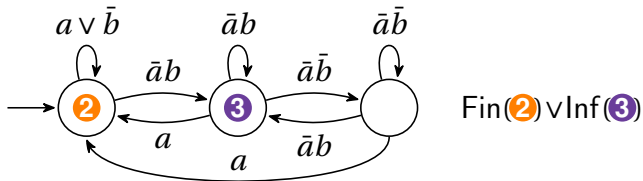
jhoafparser  
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## Resulting challenge

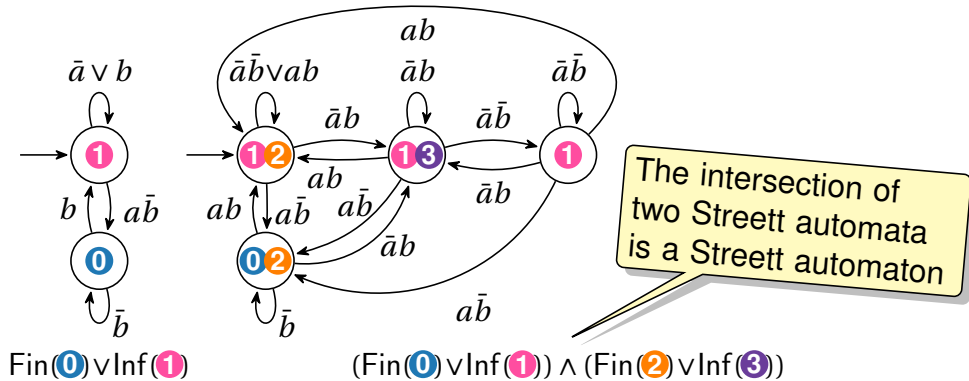
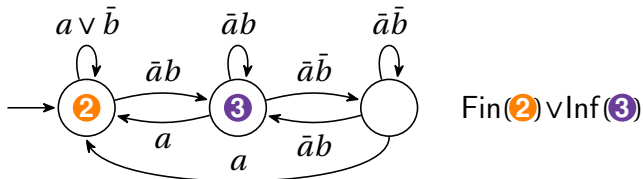
Can we build tools that process automata with arbitrary acceptance conditions?



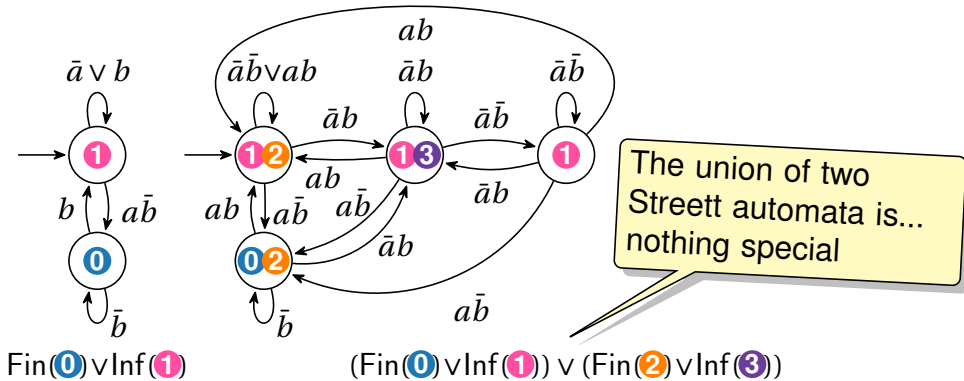
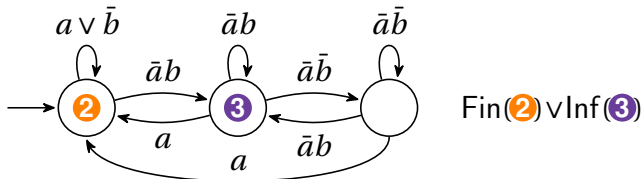
# Generic Intersections and Unions



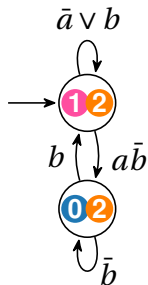
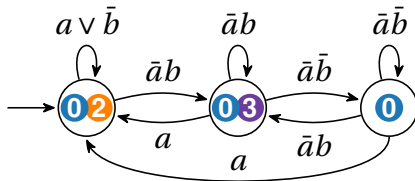
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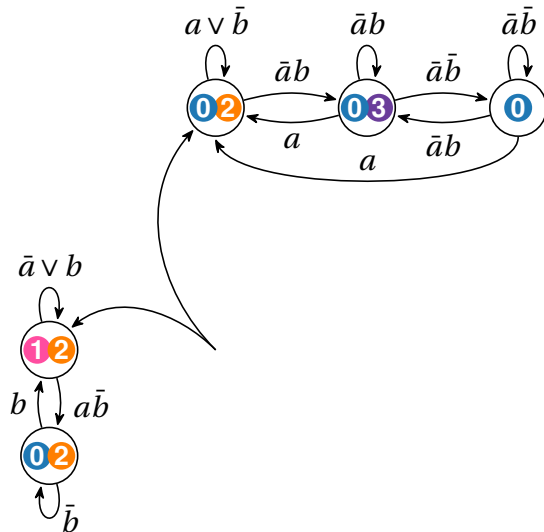
# Generic Intersections and Unions



$\text{Fin}(0) \vee \text{Inf}(1) \vee \text{Fin}(2) \vee \text{Inf}(3)$

Union using  
non-deterministic  
initial states

# Generic Intersections and Unions



Intersection  
using universal  
initial states

# Other Operations

## automata simplifications

Most of Spot's simplifications have been generalized already.

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## acceptance conversions

Many are implemented.

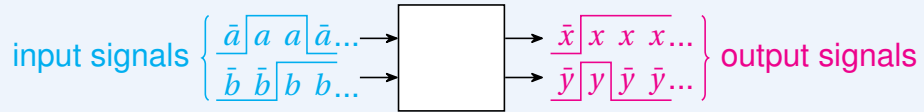
(Jupyter demo)

The background is a close-up, high-contrast image of a printed circuit board (PCB) with intricate silver-colored traces on a dark substrate. A semi-transparent blue rectangular box with rounded corners is centered on the slide, containing the text "Reactive Synthesis" in white. In the bottom right corner, there is a small blue circular icon containing the white text "18/23".

## Reactive Synthesis

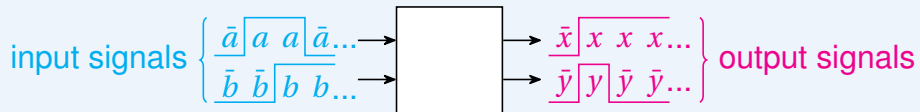
# Reactive Synthesis in a Nutshell

A reactive controller produces output as a reaction to its input



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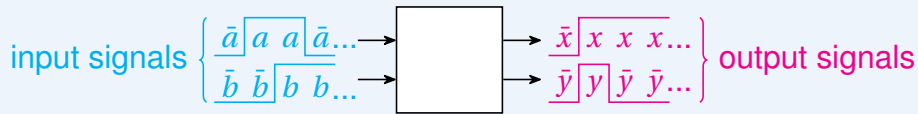
## The reactive synthesis problems

Given a specification relating **input signals** and **output signals** over time:

**Realizability:** decide if a controller exist; **Synthesis:** construct it.

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## The reactive synthesis problems

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## LTL Synthesis Competition [▶ www](#)

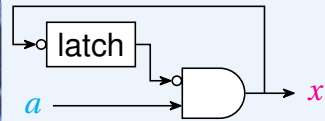
- ▶ the specification is an LTL formula, over  $\omega$ -words such as “ $\bar{a}\bar{b}\bar{x}\bar{y}; a\bar{b}xy; abx\bar{y}; \dots$ ”
- ▶ the controller should be an **And-Inverter Graph**

# Reactive Synthesis Example

## 1. LTL Spec.

$$a \leftrightarrow F(x)$$

## 6. Output AIG

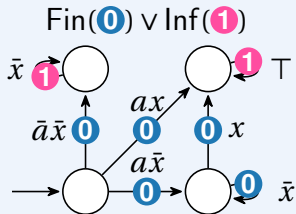


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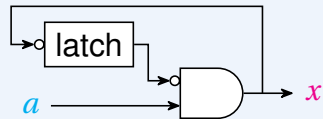
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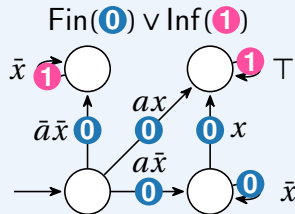


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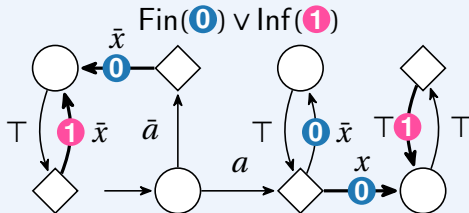
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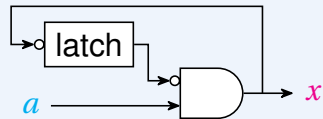
## 2. DPA



## 3. Parity Game



## 6. Output AIG

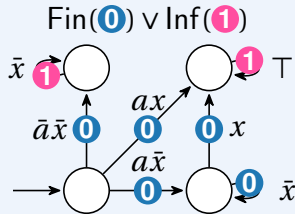


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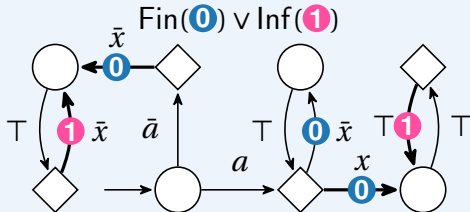
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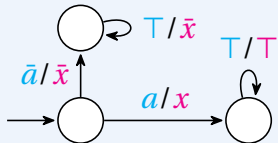
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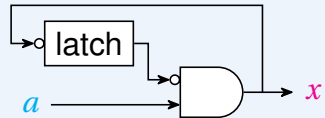
### 3. Parity Game



## 4. Winning Strategy = Mealy



## 6. Output AIG

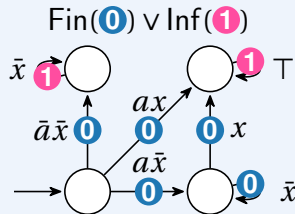


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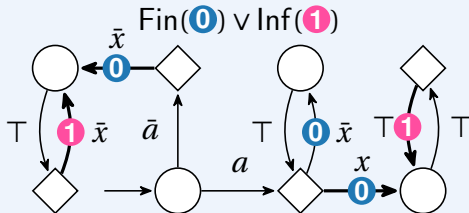
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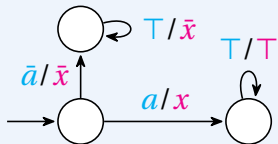
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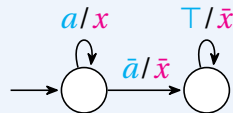
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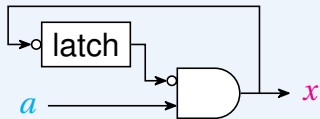
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## 5. Simplify Mealy



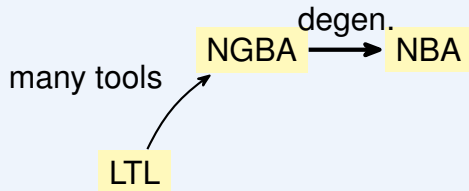
## 6. Output AIG



# How to turn an LTL formula into a DPA?



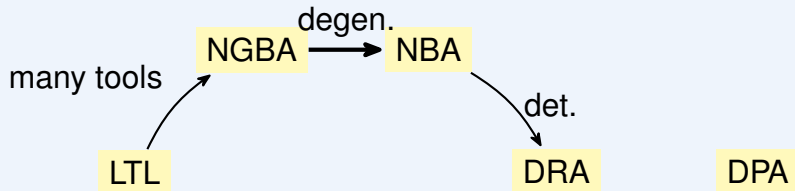
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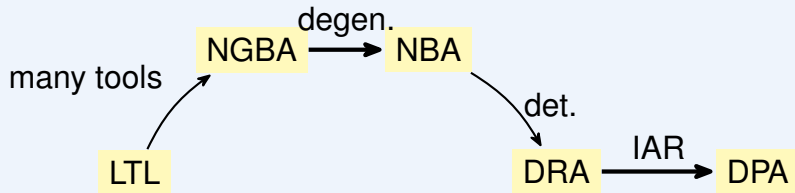
 Gastin and Oddoux. *Fast LTL to Büchi automata translation*. **CAV'01**. [▶ doi](#)

 Babiak et al. *Compositional approach to suspension and other improvements to LTL translation*. **SPIN'13**. [▶ doi](#)

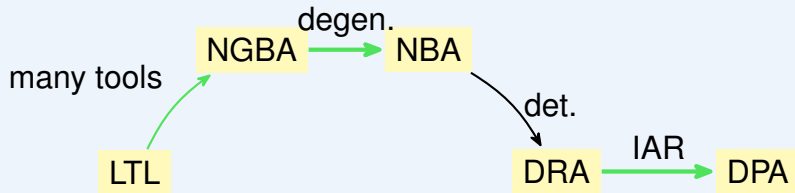
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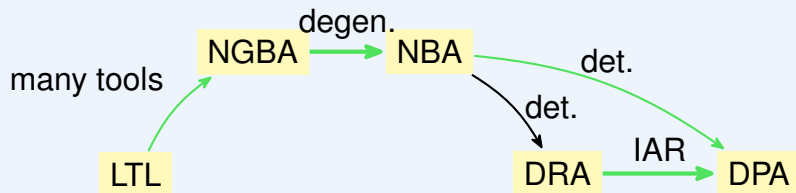


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Procedures  
implemented  
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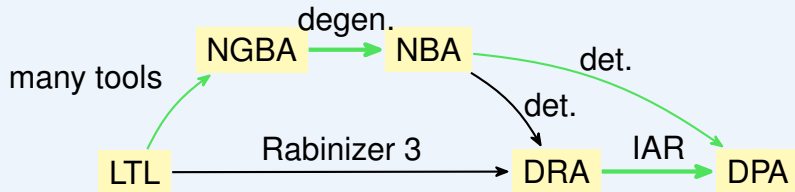
 Piterman. *From nondeterministic Büchi and Streett automata to deterministic parity automata.*

**LICS'06.** [▶ doi](#)

 Redziejewski. *An improved construction of deterministic omega-automaton using derivatives.*

**Fundamenta Informaticae**, 2012. [▶ doi](#) 

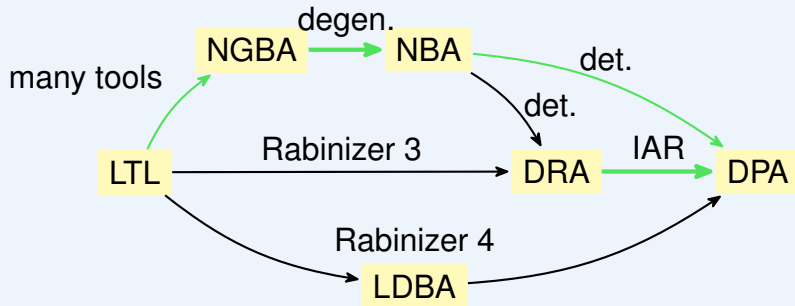
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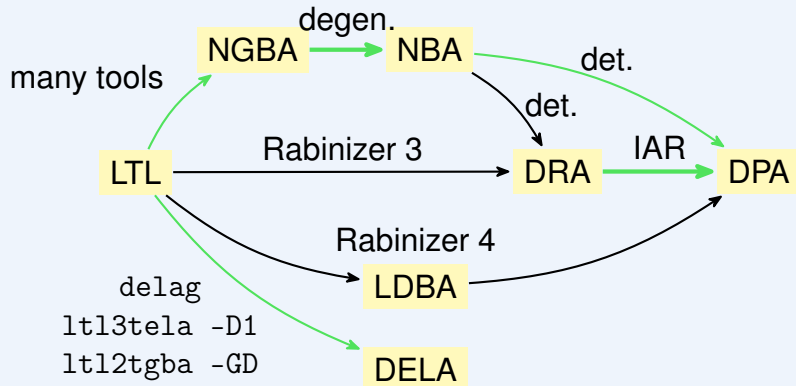
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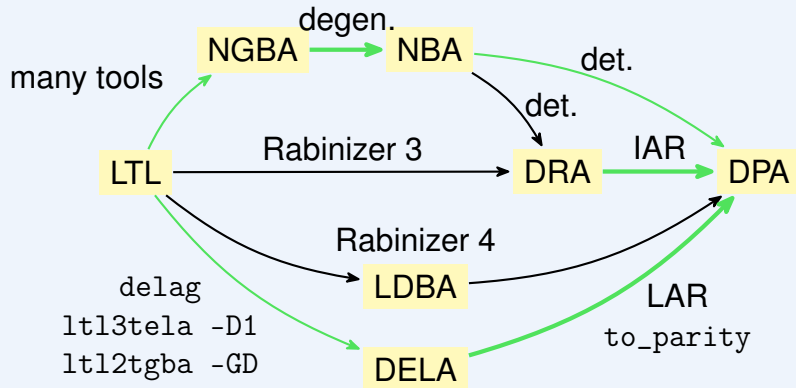
Procedures  
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Müller and Sickert. *LTL to deterministic Emerson-Lei automata*. [GandALF'17](#). [doi](#)

Major et al. *ltl3tela: LTL to small deterministic or nondeterministic Emerson-Lei automata*.

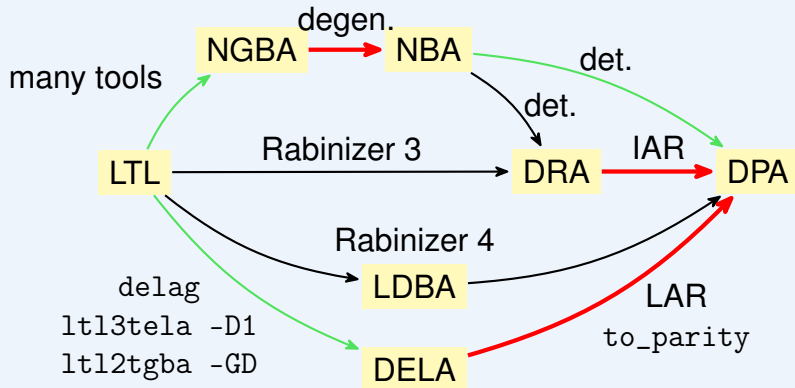
[ATVA'19](#). [doi](#)

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These **paritization**  
**procedures** are all  
improved by ACD!

 Casares, Colcombet, and Fijalkow. *Optimal transformations of games and automata using Muller conditions*. **ICALP'21**. [▶ doi](#) 

 Casares et al. *Practical applications of the Alternating Cycle Decomposition*. **TACAS'22**. [▶ doi](#) 21/23

(Jupyter demo)

# What I **did** present

## 1 Model checking library (2003–2012)

- ▶ A bug!

## 2 Platform for LTL manip. and model checking (2012–2015)

- ▶ command-line tools
- ▶ SAT-based minimization

## 3 Platform for LTL and $\omega$ -automata (2016–)

- ▶ Jupyter visualizations
- ▶ Emerson-Lei acceptance conditions

## 4 New application: Synthesis

- ▶ `ltlsynt`, ACD
- ▶ `ltlfsynt`, MTDFA