

- `abrial.96.b` [1] Jean-Raymond Abrial. *The B Book*. Cambridge University Press, October 1996.
- `aho.74` [2] Alfred V. Aho, John E. Hopcroft, and Jeffrey D. Ullman. *The Design and Analysis of Computer Algorithms*. Addison-Wesley Series in Computer Science and Information Processing. Addison-Wesley, 1974.
- `ajami.98.tacas` [3] K. Ajami, S. Haddad, and J-M. Ilié. Exploiting symmetry in linear time temporal logic model checking: One step beyond. In *First International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS'98)*, volume 1384 of *Lecture Notes in Computer Science*, pages 52–67. Springer-Verlag, 1998.
- `alpern.85.ipl` [4] Bowen Alpern and Fred B. Schneider. Defining liveness. *Information Processing Letters*, 21(4):181–185, 1985.
- `alpern.87.dc` [5] Bowen Alpern and Fred B. Schneider. Recognizing safety and liveness. *Distributed Computing*, 2(3):117–126, 1987.
- `andersen.97.bdd` [6] Henrik Reif Andersen. An introduction to binary decision diagrams. Lecture notes, October 1997.
- `armoni.02.tacas` [7] Roy Armoni, Limor Fix, Alon Flaisher, Rob Gerth, Boris Ginsburg, Tomer Kanza, Avner Landver, Sela Mador-Haim, Eli Singerman, Andreas Tiemeyer, Moshe Y. Vardi, and Yael Zbar. The forspec temporal logic: A new temporal property-specification language. In J.-P. Katoen and P. Stevens, editors, *Proceedings of the 8th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS'02)*, volume 2280 of *Lecture Notes in Computer Science*, pages 296–211. Springer-Verlag, 2002.
- `arnold.94.fts` [8] André Arnold. *Finite transition systems. Semantics of communicating systems*. Prentice-Hall, 1994.
- `baarir.04.wodes` [9] Soheib Baarir, Serge Haddad, and Jean-Michel Ilié. Exploiting partial symmetries in well-formed nets for the reachability and the linear time model checking problems. In *Proceedings of the 7th Workshop on Discrete Event Systems (WODES'04)*, pages 223–228, Reims, France, September 2004.
- `baarir.06.tr03` [10] Souheib Baarir and Alexandre Duret-Lutz. Emptiness check of powerset Büchi automata. Technical report 2006/003, Université Pierre et Marie Curie, LIP6-CNRS, Paris, France, October 2006.
- `baarir.07.acsd` [11] Souheib Baarir and Alexandre Duret-Lutz. Emptiness check of powerset Büchi automata. In *Proceedings of the 7th International Conference on Application of Concurrency to System Design (ACSD'07)*, pages 41–50. IEEE Computer Society, July 2007.
- `baarir.07.msr` [12] Souheib Baarir and Alexandre Duret-Lutz. Test de vacuité pour automates de Büchi ensemblistes avec tests d'inclusion. In *Actes du 6e Colloque Francophone sur la Modélisation des Systèmes Réactifs (MSR'07)*, October 2007. À paraître.
- `baarir.07.phd` [13] Souheib Baarir. *Exploitation des symétries partielles pour la vérification et l'évaluation de performances des systèmes finis*. PhD thesis, Université Pierre et Marie Curie (Paris 6), France, May 2007.
- `bardin.03.cav` [14] Sebastien Bardin, Alain Finkel, Jérôme Leroux, and Laure Petrucci. FAST: Fast Acceleration of Symbolic Transition systems. In *Proceedings of the 15th International Conference on Computer Aided Verification (CAV'03)*, volume 2725 of *Lecture Notes in Computer Science*, July 2003.
- `barnat.02.vcl` [15] Jiri Barnat, Lubos Brim, and Ivana Cerna. Property driven distribution of nested dfs. In *Proceedings of the Third International Workshop on Verification and Computational Logic (VCL'02)*, volume DSSE-TR-2002-5, pages 1–10, University of Southampton, UK, 2002. Declarative Systems and Software Engineering.
- `barnat.05.phd` [16] Jiří Barnat. *Distributed Memory LTL Model Checking*. PhD thesis, Faculty of Informatics, Masaryk University Brno, 2005.

- `barnat.10.sefm` [17] J. Barnat, L. Brim, and P. Rockai. Parallel partial order reduction with topological sort proviso. *Software Engineering & Formal Methods (SEFM)*, pages 222–231, 2010.
- `barnat.12.scp` [18] Jiří Barnat, Luboš Brim, and Petr Ročkai. On-the-fly parallel model checking algorithm that is optimal for verification of weak LTL properties. *Science of Computer Programming*, 77(12):1272–1288, October 2012.
- `barringer.89.avmfss` [19] Howard Barringer, Michael D. Fisher, and Graham D. Gough. Fair SMG and linear time model checking. In *Proceedings of the International Workshop on Automatic Verification Methods for Finite State Systems*, volume 407 of *Lecture Notes in Computer Science*, pages 133–150. Springer-Verlag, 1989.
- `behm.99.fm` [20] Patrick Behm, Paul Benoit, Alain Faivre, and Jean-Marc Meynadier. Météor: A successful application of B in a large project. In Jeannette M. Wing, Jim Woodcock, and Jim Davies, editors, *Proceedings of the World Congress on Formal Methods in the Development of Computing Systems (FM'99)*, volume 1708 of *Lecture Notes in Computer Science*, pages 369–387, Toulouse, France, September 1999. Springer-Verlag.
- `benari.81.popl` [21] Mordechai Ben-Ari, Zohar Manna, and Amir Pnueli. The temporal logic of branching time. In *Proceedings of the 8th ACM Symposium on Principles of Programming Languages (POPL'81)*, pages 164–176. ACM, 1981.
- `benedikt.13.tacas` [22] Michael Benedikt, Rastislav Lenhardt, and James Worrell. LTL model checking of interval markov chains. In Nir Piterman and Scott A. Smolka, editors, *19th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS'13)*, volume 7795 of *Lecture Notes in Computer Science*, pages 32–46. Springer, 2013.
- `beth.57.crise` [23] Evert Willem Beth. *La crise de la raison et la logique*. Gauthier-Villars, 1957.
- `beth.59.fom` [24] Evert Willem Beth. *The Foundations of Mathematics*. North Holland, 1959. Second edition revised in 1965.
- `bhat.95.lics` [25] Girish Bhat, Rance Cleaveland, and Orna Grumberg. Efficient on-the-fly model checking for CTL\*. In *Proceedings of the Tenth Annual IEEE Symposium on Logic in Computer Science (LICS'95)*, pages 388–397, June 1995.
- `bloem.00.fmcad` [26] Roderick Bloem, Harold N. Gabow, and Fabio Somenzi. An algorithm for strongly connected component analysis in  $n \log n$  symbolic steps. In J. W. O'Leary M. D. Aagaard, editor, *Formal Methods in Computer Aided Design (FMCAD'00)*, number 2517 in *Lecture Notes in Computer Science*, pages 37–54. Springer-Verlag, 2000.
- `bloem.01.phd` [27] Roderick Bloem. *Search Techniques and Automata for Symbolic Model Checking*. PhD thesis, University of Colorado, 2001.
- `bloem.99.cav` [28] Roderick Bloem, Kavita Ravi, and Fabio Somenzi. Efficient decision procedures for model checking of linear time logic properties. In *Proceedings of the Eleventh Conference on Computer Aided Verification (CAV'99)*, volume 1633 of *Lecture Notes in Computer Science*, pages 222–235. Springer-Verlag, 1999.
- `boigelot.01.ijcar` [29] Bernard Boigelot, Sébastien Jodogne, and Pierre Wolper. On the use of weak automata for deciding linear arithmetic with integer and real variables. In *Proceedings of the First International Joint Conference, on Automated Reasoning (IJCAR'01)*, volume 2083 of *Lecture Notes in Computer Science*, pages 611–625. Springer-Verlag, 2001.
- `bouajjani.92.scp` [30] Ahmed Bouajjani, Jean-Claude Fernandez, Nicolas Halbwachs, Pascal Raymond, and Christophe Ratel. Minimal state graph generation. *Science of Computer Programming*, 18:247–269, 1992.
- `bouquet.99.jnpc` [31] Fabrice Bouquet, Laurent Henocque, and Philippe Jégou. Énumération et représentation d'impliquants premiers. In *Actes des cinquièmes Journées Nationales sur la résolution pratique de Problèmes NP-Complets (JNPC'99)*, pages 179–188, Lyon, France, June 1999.
- `brand.00.aterm` [32] Mark G. J. van den Brand, Hayco A. de Jong, Paul Klint, and Pieter A Olivier. Efficient annotated terms. Technical Report SEN-E0003, Centrum voor Wiskunde en Informatica, February 2002.
- `brand.83.acm` [33] Daniel Brand and Pitro Zafiropulo. On communicating finite-state machines. *Journal of the ACM*, 30(2):323–342, 1983.
- `brayton.96.cav` [34] Robert Brayton, Gary D. Hachtel, Alberto Sangiovanni-vincentelli, Fabio Somenzi, Adnan Aziz, Szu tsung Cheng, and Stephen Edwards. VIS: A system for verification and synthesis. In *Proceedings of the Eighth Conference on Computer Aided Verification (CAV'96)*, volume 1102 of *Lecture Notes in Computer Science*, pages 428–432. Springer, 1996.

- `bruggeman.96.tcs` [35] Anne Brüggemann-Klein. Regular expressions into finite automata. *Theoretical Computer Science*, 120:87–98, 1996.
- `bruns.00.concur` [36] Glenn Bruns and Patrice Godefroid. Generalized model checking: Reasoning about partial state space. In C. Palamidessi, editor, *Proceedings of the 11th International Conference on Concurrency Theory (Concur'00)*, volume 1877 of *Lecture Notes in Computer Science*, pages 168–182. Springer-Verlag, 2000.
- `bryant.86.tc` [37] Randal E. Bryant. Graph-based algorithms for boolean function manipulation. *IEEE Transactions on Computers*, 35(8):677–691, August 1986.
- `bryant.92.cs` [38] Randal E. Bryant. Symbolic boolean manipulation with ordered binary-decision diagrams. *ACM Computing Surveys*, 24(3):293–318, September 1992.
- `brzozowski.80.tcs` [39] Janusz. A. Brzozowski and Ernest. L. Leiss. Finite automata, and sequential networks. *Theoretical Computer Science*, (10):19–35, 1980.
- `buchi.60.clmps` [40] J. Richard Büchi. On a decision method in restricted second order arithmetic. In *Proceedings of the International Congress on Logic, Methodology, and Philosophy of Science, Berkley, 1960*, pages 1–11. Stanford University Press, 1962. Republished in [171].
- `burch.91.slcd` [41] Jerry R. Burch, Edmund M. Clarke, Kenneth L. McMillan, David L. Dill, and L.J. Hwang. Symbolic model checking:  $10^{20}$  states and beyond. In *Proceedings of the Fifth Annual IEEE Symposium on Logic in Computer Science*, pages 1–33, Washington, D.C., 1990. IEEE Computer Society Press.
- `capra.00.mtcs` [42] Lorenzo Capra, Claudiu Dutheillet, Giovanni Franceschinis, and Jean-Michel Ilié. Exploiting partial symmetries for markov chain aggregation. In Flavio Corradini and Paola Inverardi, editors, *First International Workshop on Models for Time-Critical Systems (MTCS'2000)*, volume 39 of *Electronic Notes in Theoretical Computer Science*. Elsevier Science Publishers, December 2000.
- `cerna.03.mfcs` [43] Ivana Černá and Radek Pelánek. Relating hierarchy of temporal properties to model checking. In Branislav Rován and Peter Vojtáš, editors, *Proceedings of the 28th International Symposium on Mathematical Foundations of Computer Science (MFCS'03)*, volume 2747 of *Lecture Notes in Computer Science*, pages 318–327, Bratislava, Slovak Republic, August 2003. Springer-Verlag.
- `chang.92.icalp` [44] Edward Y. Chang, Zohar Manna, and Amir Pnueli. Characterization of temporal property classes. In *Proceedings of the 19th International Colloquium on Automata, Languages and Programming (ICALP'92)*, pages 474–486, London, UK, 1992. Springer-Verlag.
- `chiola.90.atpn` [45] Giovanni Chiola, Claude Dutheillet, Giuliana Franceschinis, and Serge Haddad. On well-formed coloured nets and their symbolic reachability graph. In K. Jensen and G. Rozenberg, editors, *Proceedings of the 11th International Conference on Application and Theory of Petri Nets. Paris, France, June 1990. Reprinted in High-Level Petri Nets. Theory and Application*. Springer-Verlag, 1991.
- `chiola.93.toc` [46] Giovanni Chiola, Claude Dutheillet, Giuliana Franceschinis, and Serge Haddad. Stochastic well-formed coloured nets for symmetric modelling applications. *IEEE Transactions on Computers*, 42(11):1343–1360, November 1993.
- `chiola.95.pe` [47] Giovanni Chiola, Giuliana Franceschinis, Rossano Gaeta, and Marina Ribaud. GreatSPN 1.7: Graphical Editor and Analyzer for Timed and Stochastic Petri Nets. *Performance Evaluation*, 24(1–2):47–68, 1995.
- `chiola.97.tcs` [48] Giovanni Chiola, Claude Dutheillet, Giuliana Franceschinis, and Serge Haddad. A symbolic reachability graph for coloured Petri nets. *Theoretical Computer Science*, 176(1–2):39–65, April 1997.
- `choueka.74.jcss` [49] Yaacov Choueka. Theories of automata on  $\omega$ -tapes: A simplified approach. *Journal of Computer and System Sciences*, 8(2):117–141, April 1974.
- `ciardo.00.icatpn` [50] Gianfranco Ciardo, Gerardo Lüttgen, and Radu Siminiceanu. Efficient symbolic state-space construction for asynchronous systems. In Mogens Nielsen and Dan Simpson, editors, *Proceedings of the 21st International Conference on Application and Theory of Petri Nets (ICATPN'00)*, volume 1825 of *Lecture Notes in Computer Science*, pages 103–122. Springer-Verlag, 2000.
- `cichon.09.depcos` [51] Jacek Cichoń, Adam Czubak, and Andrzej Jasiński. Minimal Büchi automata for certain classes of LTL formulas. In *Proceedings of the Fourth International Conference on Dependability of Computer Systems (DEPCOS'09)*, pages 17–24. IEEE Computer Society, 2009.

- `cimatti.02.cav` [52] Alessandro Cimatti, Edmund Clarke, Enrico Giunchuglia, Fausto Giunchiglia, Marco Pistore, Macro Roveri, Roberto Sebastiani, and Armando Tacchella. Nusmv 2: An opensource tool for symbolic model checking. In E. Brinksma and K. Guldstrand Larsen, editors, *Proceedings of the 14th International Conference on Computer Aided Verification (CAV'02)*, volume 2404 of *Lecture Notes in Computer Science*, pages 359–364, Copenhagen, Denmark, July 2002. Springer-Verlag.
- `clarke.00.mc` [53] Edmund M. Clarke, Orna Grumberg, and Doron A. Peled. *Model Checking*. The MIT Press, 2000.
- `clarke.01.har` [54] Edmund M. Clarke and Bernd-Holger Schlingloff. Model checking. In Alan Robinson and Andrei Voronkov, editors, *Handbook of Automated Reasoning*, chapter 4, pages 1367–1522. Elsevier Science Publishers, 2001.
- `clarke.02.lics` [55] Edmund M. Clarke, Somesh Jha, Yuan Lu, and Helmut Veith. Proceedings of the 17th ieev symposium on logic in computer science (lics'02). In *LICS*, pages 19–29, Copenhagen, Denmark, July 2002. IEEE Computer Society.
- `clarke.83.popl` [56] Edmund M. Clarke, E. Allen Emerson, and A. Prasad Sistla. Automatic verification of finite state concurrent system using temporal logic specifications: a practical approach. In *Proceedings of the 10th ACM SIGACT-SIGPLAN symposium on Principles of programming languages (POPL'83)*, pages 117–126. ACM Press, 1983.
- `clarke.94.tr204` [57] Edmund M. Clarke, Orna Grumberg, Kenneth L. McMillan, and Xudong Zhao. Efficient generation of counterexamples and witness in symbolic model checking. Technical report CMU-CS-94-204, Carnegie Mellon University, School of Compute Science, Pittsburg, October 1994. Later republished as [58].
- `clarke.95.dac` [58] Edmund M. Clarke, Orna Grumberg, Kenneth L. McMillan, and Xudong Zhao. Efficient generation of counterexamples and witness in symbolic model checking. In *Proceedings of the 32nd ACM/IEEE Design Automation Conference (DAC'95)*, pages 427–432, San Francisco, California, USA, June 1995. ACM Press.
- `clarke.96.acm` [59] Edmund M. Clarke and Jeannette M. Wing. Formal methods: State of the art and future. *ACM Computing Surveys*, 28(4):626–643, 1996.
- `clarke.98.cav` [60] Edmund M. Clarke, E. Allen Emerson, Somesh Jha, and A. Prasad Sistla. Efficient decision procedures for model checking of linear time logic properties. In *Proceedings of the Tenth Conference on Computer Aided Verification (CAV'98)*, volume 1427 of *Lecture Notes in Computer Science*, pages 147–158. Springer-Verlag, 1998.
- `clarke.98.tableaux` [61] Edmund M. Clarke. Model checking: Historical perspective and example. In *Proceedings of Analytic Tableaux and Related Methods (TABLEAUX'98)*, volume 1397 of *Lecture Notes in Computer Science*, pages 18–24. Springer-Verlag, 1998.
- `cormen.01.algo` [62] Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest, and Clifford Stein. *Introduction to Algorithms*. The MIT Press, 2nd edition, 2001.
- `coudert.94.tllm` [63] Olivier Coudert. Two-level logic minimization: an overview. *Integration, the VLSI journal*, 17(2):97–140, October 1994.
- `courcoubetis.90.cav` [64] Costas Courcoubetis, Moshe Y. Vardi, Pierre Wolper, and Mihalis Yannakakis. Memory-efficient algorithm for the verification of temporal properties. In Edmund M. Clarke and Robert P. Kurshan, editors, *Proceedings of the 2nd international workshop on Computer Aided Verification (CAV'90)*, volume 531 of *Lecture Notes in Computer Science*, pages 233–242. Springer-Verlag, 1991.
- `courcoubetis.92.fmsd` [65] Costas Courcoubetis, Moshe Y. Vardi, Pierre Wolper, and Mihalis Yannakakis. Memory-efficient algorithm for the verification of temporal properties. *Formal Methods in System Design*, 1:275–288, 1992.
- `couvreur.00.icatpn` [66] Jean-Michel Couvreur, Sébastien Grivet, and Denis Poitrenaud. Designing a LTL model-checker based on unfolding graphs. In *Proceedings of the 21th International Conference on Applications and Theory of Petri Nets (ICATPN'00)*, volume 2075 of *Lecture Notes in Computer Science*, Aarhus, Denmark, June 2000. Springer-Verlag.
- `couvreur.00.lacim` [67] Jean-Michel Couvreur. Un point de vue symbolique sur la logique temporelle linéaire. In Pierre Leroux, editor, *Actes du Colloque LaCIM 2000*, volume 27 of *Publications du LaCIM*, pages 131–140, Montréal, August 2000. Université du Québec à Montréal.

- `couvreur.02.ictpn` [68] Jean-Michel Couvreur, Emmanuelle Encrenaz, Emmanuel Paviot-Adet, Denis Poitrenaud, and Pierre-André Wacrenier. Data Decision Diagrams for Petri net analysis. In J. Esparza and C. Lakos, editors, *Proceedings of the 23rd International Conference on Applications and Theory of Petri Nets (ICATPN'02)*, volume 2360 of *Lecture Notes in Computer Science*, pages 101–120. Springer-Verlag, 2002.
- `couvreur.04.hdr` [69] Jean-Michel Couvreur. Contribution à l’algorithmique de la vérification. Habilitation à diriger des recherches, ENS Cachan, 2004.
- `couvreur.05.spin` [70] Jean-Michel Couvreur, Alexandre Duret-Lutz, and Denis Poitrenaud. On-the-fly emptiness checks for generalized Büchi automata. In Patrice Godefroid, editor, *Proceedings of the 12th International SPIN Workshop on Model Checking of Software (SPIN'05)*, volume 3639 of *Lecture Notes in Computer Science*, pages 143–158. Springer, August 2005.
- `couvreur.99.fm` [71] Jean-Michel Couvreur. On-the-fly verification of temporal logic. In Jeannette M. Wing, Jim Woodcock, and Jim Davies, editors, *Proceedings of the World Congress on Formal Methods in the Development of Computing Systems (FM'99)*, volume 1708 of *Lecture Notes in Computer Science*, pages 253–271, Toulouse, France, September 1999. Springer-Verlag.
- `dallien.06.aejm` [72] Jeffrey Dallien and Wendy MacCaull. Automated recognition of stutter-invariant LTL formulas. *Atlantic Electronic Journal of Mathematics*, (1):56–74, 2006.
- `daniele.99.cav` [73] Marco Daniele, Fausto Giunchiglia, and Moshe Y. Vardi. Improved automata generation for Linear Temporal Logic. In N. Halbwachs and D. Peled, editors, *Proceedings of the 11th International Conference on Computer Aided Verification (CAV'99)*, volume 1633 of *Lecture Notes in Computer Science*, pages 249–260. Springer-Verlag, 1999.
- `daniele.99.tr` [74] Marco Daniele, Fausto Giunchiglia, and Moshe Y. Vardi. Improved automata generation for Linear Temporal Logic. Technical report, ITC-IRSC, 1999. Later republished as [73].
- `dax.07.atva` [75] Christian Dax, Jochen Eisinger, and Felix Klaedtke. Mechanizing the powerset construction for restricted classes of  $\omega$ -automata. In Kedar S. Namjoshi, Tomohiro Yoneda, Teruo Higashino, and Yoshio Okamura, editors, *Proceedings of the 5th International Symposium on Automated Technology for Verification and Analysis (ATVA'07)*, volume 4762 of *Lecture Notes in Computer Science*. Springer, October 2007.
- `dejong.10.mcs` [76] M. de Jonge and T.C. Ruys. The spinja model checker. *Proceedings of the 17th international SPIN conference on Model checking software*, pages 124–128, 2010.
- `diaz.01.ic2` [77] Michel Diaz. *Réseaux de Petri, Modèles fondamentaux*. Traité IC2, série Informatique et systèmes d’information. Hermes Science, June 2001.
- `dijkstra.73.ewd376` [78] Edsger Wybe Dijkstra. EWD 376: Finding the maximum strong components in a directed graph. <http://www.cs.utexas.edu/users/EWD/ewd03xx/EWD376.PDF>, May 1973.
- `dijkstra.76.dop` [79] Edsger Wybe Dijkstra. Finding the maximal strong components in a directed graph. In *A Discipline of Programming*, chapter 25, pages 192–200. Prentice-Hall, 1976.
- `divine.10.hibi` [80] J. Barnat, L. Brim, M. Češka, and P. Ročkal. Divine: Parallel distributed model checker (tool paper). In *Parallel and Distributed Methods in Verification and High Performance Computational Systems Biology (HiBi/PDMC 2010)*, pages 4–7. IEEE, 2010.
- `duret.03.msc` [81] Alexandre Duret-Lutz and Rachid Rebiha. Spot : une bibliothèque de vérification de propriétés de logique temporelle à temps linéaire. Master’s thesis, DEA Systèmes Informatiques Répartis, Université de Paris 6, September 2003.
- `duret.04.mascots` [82] Alexandre Duret-Lutz and Denis Poitrenaud. SPOT: an Extensible Model Checking Library using Transition-based Generalized Büchi Automata. In *Proceedings of the 12th IEEE/ACM International Symposium on Modeling, Analysis, and Simulation of Computer and Telecommunication Systems (MASCOTS'04)*, pages 76–83, Volendam, The Netherlands, October 2004. IEEE Computer Society Press.
- `duret.07.phd` [83] Alexandre Duret-Lutz. *Contributions à l’approche automate pour la vérification de propriétés de systèmes concurrents*. PhD thesis, Université Pierre et Marie Curie (Paris 6), July 2007.
- `duret.11.atva` [84] Alexandre Duret-Lutz, Kais Klai, Denis Poitrenaud, and Yann Thierry-Mieg. Self-loop aggregation product — a new hybrid approach to on-the-fly LTL model checking. In *Proceedings of the 9th International Symposium on Automated Technology for Verification and Analysis (ATVA'11)*, volume 6996 of *Lecture Notes in Computer Science*, pages 336–350, Taipei, Taiwan, October 2011. Springer-Verlag.

- duret.11.vecos [85] Alexandre Duret-Lutz. LTL translation improvements in Spot. In *Proceedings of the 5th International Workshop on Verification and Evaluation of Computer and Communication Systems (VECoS'11)*, Electronic Workshops in Computing, Tunis, Tunisia, September 2011. British Computer Society.
- dwyer.98.fmsp [86] Matthew B. Dwyer, George S. Avrunin, and James C. Corbett. Property specification patterns for finite-state verification. In Mark Ardis, editor, *Proceedings of the 2nd Workshop on Formal Methods in Software Practice (FMSP'98)*, pages 7–15, New York, March 1998. ACM Press.
- edelkamp.01.spin [87] Stefan Edelkamp, Alberto Lluch Lafuente, and Stefan Leue. Directed explicit model checking with HSF-SPIN. In *Proceedings of the 8th international Spin workshop on model checking of software (SPIN'01)*, volume 2057 of *Lecture Notes in Computer Science*, pages 57–79. Springer-Verlag, 2001.
- edelkamp.04.sttt [88] Stefan Edelkamp, Stefan Leue, and Alberto Lluch-Lafuente. Directed explicit-state model checking in the validation of communication protocols. *STTT*, 5(2–3):247–267, 2004.
- edvardsson.99.ccsse [89] Jon Edvardsson. A survey on automatic test data generation. In *Proceedings of the Second Conference on Computer Science and Engineering (CCSSE'99)*, 1999.
- ehlers.10.spin [90] Rüdiger Ehlers and Bernd Finkbeiner. On the virtue of patience: minimizing Büchi automata. In *Proceedings of the 17th international SPIN conference on Model checking software (SPIN'10)*, volume 6349 of *Lecture Notes in Computer Science*, pages 129–145. Springer, September 2010.
- eker.03.spin [91] Steven Eker, José Meseguer, and Ambarish Sridharanarayanan. The Maude LTL model checker and its implementation. In *Proceedings of the 10th International SPIN Workshop on Model Checking Software (SPIN'03)*, volume 2648 of *Lecture Notes in Computer Science*, pages 230–2–34, Portland, Oregon, USA, May. Springer.
- emerson.84.stoc [92] E. Allen Emerson and A. Prasad Sistla. Deciding branching time logic. In *Proceedings of the 16th annual ACM symposium on Theory of computing (STOC'84)*, pages 14–24. ACM Press, 1984.
- emerson.87.scp [93] E. Allen Emerson and Chin-Laung Lei. Modalities for model checking: Branching time logic strikes back. *Science of Computer Programming*, 8(3):275–306, June 1987.
- emerson.90.cwrp [94] E. Allen Emerson. The role of Büchi’s automata in computing science. In Lane and Siefkes [171].
- emerson.96.banff [95] E. Allen Emerson. Automated temporal reasoning about reactive systems. In *Proceedings of the 8th Banff Higher Order Workshop*, volume 1043 of *Lecture Notes in Computer Science*, pages 41–101, Banff, Alberta, Canada, 1996. Springer-Verlag.
- emerson.97.dcfm [96] E. Allen Emerson. Model checking and the mu-calculus. In N. Immerman and P. G. Kolaitis, editors, *Descriptive Complexity and Finit Models*, volume 31 of *DIMACS: Series in Discrete Mathematics and Theoretical Computer Science*. American Mathematical Society, 1997.
- esparza.02.fm [97] Javier Esparza, Stefan Römer, and Walter Vogler. An improvement of mcmillan’s unfolding algorithm. *Formal Methods in System Design*, 20(3):285–310, 2002.
- esparza.01.movep [98] Javier Esparza. Verification of systems with an infinite state space. In F. Cassez, C. Jard, B. Rozoy, and M. Dermot, editors, *Proceedings of the 4th Summer School on Modeling and Verification of Parallel Processes (MOVEP'00)*, volume 2067 of *Lecture Notes in Computer Science*, pages 183–186. Springer-Verlag, 2001.
- etessami.00.concur [99] Kousha Etessami and Gerard J. Holzmann. Optimizing Büchi automata. In C. Palamidessi, editor, *Proceedings of the 11th International Conference on Concurrency Theory (Concur'00)*, volume 1877 of *Lecture Notes in Computer Science*, pages 153–167, Pennsylvania, USA, 2000. Springer-Verlag.
- etessami.00.ippl [100] Kousha Etessami. A note on a question of Peled and Wilke regarding stutter-invariant LTL. *Information Processing Letters*, 75(6):261–263, 2000.
- etessami.01.alp [101] Kousha Etessami, Thomas Wilke, and Rebecca A. Schuller. Fair simulation relations, parity games, and state space reduction for Büchi automata. In Fernando Orejas, Paul G. Spirakis, and Jan van Leeuwen, editors, *Proceedings of the 28th international colloquium on Automata, Languages and Programming*, volume 2076 of *Lecture Notes in Computer Science*, pages 694–707, Crete, Greece, July 2001. Springer-Verlag.

- `etessami.99.cav` [102] Kousha Etessami. Stutter-invariant languages,  $\omega$ -automata, and temporal logic. In N. Halbwachs and D. Peled, editors, *Proceedings of the 11th International Conference on Computer Aided Verification (CAV'99)*, volume 1633 of *Lecture Notes in Computer Science*, pages 236–248. Springer-Verlag, 1999.
- `evangelista.10.sttt` [103] S. Evangelista and C. Pajault. Solving the ignoring problem for partial order reduction. *International Journal on Software Tools for Technology Transfer*, 12(2):155–170, 2010.
- `even.81.acm` [104] Shimon Even and Yossi Shiloach. An on-line edge-deletion problem. *Journal of the Association for Computing Machinery*, 28(1):1–4, 1981.
- `finkbeiner.01.tcs` [105] Bernd Finkbeiner and Henry B. Sipma. Checking finite traces using alternating automata. In *Proceedings of the First International Workshop on Runtime Verification*, volume 55 of *Electronic Notes in Theoretical Computer Science*. Elsevier, 2001.
- `finkel.02.tr14` [106] Alain Finkel and Jérôme Leroux. How to compose presburger-accelerations: Applications to broadcast protocols. Technical report, Laboratoire Spécification et Vérification, ENS Cachan, September 2002.
- `fisher.92.fac` [107] Michael Fisher. A model checker for linear time temporal logic. *Formal Aspects of Computing*, 4(3):299–319, 1992.
- `fisler.01.tacas` [108] Kathi Fisler, Ranan Fraer, Gila Kamhi, Moshe Y. Vardi, and Zijiang Yang. Is there a best symbolic cycle-detection algorithm? In *Proceedings of the fourth International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS'01)*, volume 2031 of *LNCS*, pages 420–434. Springer-Verlag.
- `fitting.96` [109] Melving Fitting. *First-Order Logic and Automated Theorem Proving*. Springer-Verlag, 2nd edition, 1996.
- `francez.89.fairness` [110] Nissim Francez. *Fairness*. Springer-Verlag, 1986.
- `friedgut.04.atva` [111] Ehud Friedgut, Orna Kupferman, and Moshe Y. Vardi. Büchi complementation made tighter. In *Proceedings of the 2nd International Symposium on Automated Technology for Verification and Analysis (ATVA'04)*, volume 3299 of *Lecture Notes in Computer Science*, pages 64–78. Springer, 2004.
- `fritz.02.tcs` [112] Carsten Fritz and Thomas Wilke. State space reductions for alternating Büchi automata: Quotienting by simulation equivalences. In Manindra Agrawal and Anil Seth, editors, *Proceedings of the 22nd Conference on Foundations of Software Technology and Theoretical Computer Science (FSTTCS'2002)*, volume 2556 of *Lecture Notes in Computer Science*, pages 157–168, Kanpur, India, 2002.
- `fritz.03.ciaa` [113] Carsten Fritz. Constructing Büchi automata from linear temporal logic using simulation relations for alternating Büchi automata. In Oscar H. Ibarra and Zhe Dang, editors, *Proceedings of the 8th International Conference on Implementation and Application of Automata (CIAA'03)*, volume 2759 of *Lecture Notes in Computer Science*, pages 35–48, Santa Barbara, California, July 2003. Springer-Verlag.
- `fritz.05.lpar` [114] Carsten Fritz. Concepts of automata construction from LTL. In G. Sutcliffe and A. Voronkov, editors, *Proceedings of the 12th International Conference on Logic for Programming, Artificial Intelligence, and Reasoning (LPAR'05)*, volume 3835 of *Lecture Notes in Artificial Intelligence*, pages 728–742, Montego Bay, Jamaica, 2005. Springer.
- `gastin.01.cav` [115] Paul Gastin and Denis Oddoux. Fast LTL to Büchi automata translation. In G. Berry, H. Comon, and A. Finkel, editors, *Proceedings of the 13th International Conference on Computer Aided Verification (CAV'01)*, volume 2102 of *Lecture Notes in Computer Science*, pages 53–65, Paris, France, 2001. Springer-Verlag.
- `gastin.04.spin` [116] Paul Gastin, Pierre Moro, and Marc Zeitoun. Minimization of counterexamples in SPIN. In S. Graf and L. Mounier, editors, *Proceedings of the 11th International SPIN Workshop on Model Checking of Software (SPIN'04)*, volume 2989 of *Lecture Notes in Computer Science*, pages 92–108, April 2004.
- `geldenhuis.04.tacas` [117] Jaco Geldenhuis and Antti Valmari. Tarjan's algorithm makes on-the-fly LTL verification more efficient. In Kurt Jensen and Andreas Podelski, editors, *Proceedings of the 10th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS'04)*, volume 2988 of *Lecture Notes in Computer Science*, pages 205–219. Springer, 2004.

- `geldenhuys.05.tcs` [118] Jaco Geldenhuys and Antti Valmari. More efficient on-the-fly LTL verification with Tarjan's algorithm. *Theoretical Computer Science*, 345(1):60–82, November 2005. Conference paper selected for journal publication.
- `geldenhuys.06.spin` [119] Jaco Geldenhuys and Henri Hansen. Larger automata and less work for LTL model checking. In *Proceedings of the 13th International SPIN Workshop (SPIN'06)*, volume 3925 of *Lecture Notes in Computer Science*, pages 53–70. Springer, 2006.
- `geldenhuys.09.atva` [120] Jaco Geldenhuys, Henri Hansen, and Antti Valmari. Exploring the scope for partial order reduction. In Zhiming Liu and Anders P. Ravn, editors, *Proceedings of the 7th International Symposium on Automated Technology for Verification and Analysis (ATVA'09)*, volume 5799 of *Lecture Notes in Computer Science*, pages 39–53. Springer-Verlag, 2009.
- `gerth.95.pstv` [121] Rob Gerth, Doron Peled, Moshe Y. Vardi, and Pierre Wolper. Simple on-the-fly automatic verification of linear temporal logic. In *Proceedings of the 15th Workshop on Protocol Specification Testing and Verification (PSTV'95)*, pages 3–18, Warsaw, Poland, 1996. Chapman & Hall.
- `giannakopoulou.01.tr` [122] Dimitra Giannakopoulou and Flavio Lerda. Efficient translation of LTL formulæ into Büchi automata. Technical Report 01.29, Research Institute for Advanced Computer Science, June 2001. Later republished as [123].
- `giannakopoulou.02.forte` [123] Dimitra Giannakopoulou and Flavio Lerda. From states to transitions: Improving translation of LTL formulæ to Büchi automata. In D.A. Peled and M.Y. Vardi, editors, *Proceedings of the 22nd IFIP WG 6.1 International Conference on Formal Techniques for Networked and Distributed Systems (FORTE'02)*, volume 2529 of *Lecture Notes in Computer Science*, pages 308–326, Houston, Texas, November 2002. Springer-Verlag.
- `godefroid.91.cav` [124] P. Godefroid. Using partial orders to improve automatic verification methods. In *Computer-Aided Verification*, pages 176–185. Springer, 1991.
- `godefroid.93.cav` [125] P. Godefroid and D. Pirotin. Refining dependencies improves partial-order verification methods. In *Computer Aided Verification*, pages 438–449. Springer, 1993.
- `godefroid.93.fmsd` [126] P. Godefroid and P. Wolper. Using partial orders for the efficient verification of deadlock freedom and safety properties. *Formal Methods in System Design*, 2(2):149–164, 1993.
- `godefroid.93.pstv` [127] Patrice Godefroid and Gerard J. Holzmann. On the verification of temporal properties. In André A. S. Danthine, Guy Leduc, and Pierre Wolper, editors, *Proceedings of the 13th IFIP TC6/WG6.1 International Symposium on Protocol Specification, Testing, and Verification (PSTV'93)*, volume C-16 of *IFIP Transactions*, pages 109–124, Liege, Belgium, May 1993. North-Holland.
- `godefroid.95.fms` [128] Patrice Godefroid, Gerard Holzmann, and Didier Pirotin. State-space caching revisited. *Formal Methods in System Design*, 7(3):227–241, November 1995.
- `godefroid.96.phdlns` [129] Patrice Godefroid. *Partial-Order Methods for the Verification of Concurrent Systems: An Approach to the State-Explosion Problem*, volume 1032 of *Lecture Notes in Computer Science*. Springer-Verlag, 1996.
- `grdel.02` [130] Erich Grädel, Wolfgang Thomas, and Thomas Wilke. *Automata, logic, and infinite games*, volume 2500 of *Lecture Notes in Computer Science*. Springer-Verlag, 2002.
- `gurumurthy.03.charme` [131] Sankar Gurumurthy, Orna Kupferman, Fabio Somenzi, and Moshe Y. Vardi. On complementing nondeterministic Büchi automata. In *Proceedings of the 12th Advanced Research Working Conference on Correct Hardware Design and Verification Methods (CHARME'03)*, volume 2860 of *Lecture Notes in Computer Science*, pages 96–110. Springer-Verlag, 2003.
- `haddad.00.forte` [132] Serge Haddad, Jean-Michel Ilié, and Khalil Ajami. A model checking method for partially symmetric systems. In *Proceedings of the International Conference on Formal Description techniques: theory, application and tools (FORTE-PSTV'00)*, October 2000.
- `haddad.01.mf4` [133] Serge Haddad. Décidabilité et complexité de problèmes de réseaux de Petri. In Michel Diaz, editor, *Réseaux de Petri, Modèles fondamentaux*, Traité IC2, série Informatique et systèmes d'information, chapitre 4. Hermes Science, June 2001.
- `haddad.02.smc` [134] Serge Haddad, Jean-Michel Ilié, and Kais Klai. An incremental verification technique using decomposition of Petri nets. In *Proceedings of the IEEE International Conference on Systems, Man and Cybernetics*, October 2002.
- `haddad.03.diaz` [135] Serge Haddad and Jean-Michel Ilié. *Symétries et logique temporelle*, chapter 4. Traité IC2, série Informatique et systèmes d'information. Hermes Science, February 2003.



- [136] Serge Haddad and François Vernadat. Vérification de propriétés spécifiques. In Michel Diaz, editor, *Vérification et mise en œuvre des réseaux de Petri*, Traité IC2, série Informatique et systèmes d'information, chapter 1. Hermes Science, January 2003.
- [137] Serge Haddad, Jean-Michel Ilié, and Kais Klai. Design and evaluation of a symbolic and abstraction-based model checker. In F. Wang, editor, *Proceedings of the 2nd International Symposium on Automated Technology for Verification and Analysis (ATVA'04)*, volume 3299 of *Lecture Notes in Computer Science*, pages 198–210, National Taiwan University, Taiwan, October 2004. Springer.
- [138] Serge Haddad, Jean-Michel Ilié, Mohamed Taghelit, and Belhassen Zouari. Symbolic marking graph and partial symmetries. In *Proceedings of the 16th International Conference on Application and Theory of Petri Nets (ICATPN'95)*, pages 238–257, Torino, Italy, 1995.
- [139] Moshe Y. Vardi Joseph Y. Halpern. Model checking vs. theorem proving: A manifesto. In *Proceedings of the 2nd International Conference on Principles of Knowledge Representation and Reasoning (KR'91)*, pages 325–334, Cambridge, MA, USA, April 1991. Morgan Kaufmann Publishers.
- [140] Moritz Hammer, Alexander Knapp, and Stephan Merz. Truly on-the-fly LTL model checking. In Nicolas Halbwachs and Lenore Zuck, editors, *Proceedings of the 11th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS'05)*, volume 3440 of *Lecture Notes in Computer Science*, Edinburgh, Scotland, UK, April 2005. Springer.
- [141] Henri Hansen, Wojciech Penczek, and Antti Valmari. Stuttering-insensitive automata for on-the-fly detection of livelock properties. In Rance Cleaveland and Hubert Garavel, editors, *Proceedings of the 7th International ERCIM Workshop in Formal Methods for Industrial Critical Systems (FMICS'02)*, volume 66(2) of *Electronic Notes in Theoretical Computer Science*, Málaga, Spain, July 2002. Elsevier.
- [142] Monika Heiner, David Gilbert, and Robin Donaldson. Petri nets for systems and synthetic biology. In *Proceedings of the 8th International School on Formal Methods for the Design of Computer (SFM'08)*, volume 5016 of *Lecture Notes in Computer Science*, pages 215–264. Springer, 2008.
- [143] Keijo Heljanko. *Combining Symbolic and Partial Order Methods for Model Checking 1-Safe Petri Nets*. PhD thesis, Helsinki University of Technology, March 2002.
- [144] Gerard J. Holzmann. Software model checking. NATO Summer School, pages 309–355, Marktoberdorf, Germany, August 2000. IOS Press Computer and System Sciences.
- [145] Gerard J. Holzmann. *The Spin Model Checker: Primer and Reference Manual*. Addison-Wesley, 2003.
- [146] Gerard J. Holzmann and Dragan Bošnački. Multi-core model checking with SPIN. In *1st Workshop on Tools, Operating Systems and Programming Models for Developing Reliable Systems (TOPMoDeLS'07)*.
- [147] Gerard J. Holzmann. An improved protocol reachability analysis technique. *Software Practice and Experience*, 18(2):137–161, 1988.
- [148] Gerard J. Holzmann and Doron Peled. An improvement in formal verification. In *Proceeding of the 7th IFIP WG 6.1 International Conference on Formal Description Techniques (FORTE'94)*, volume 6 of *IFIP Conference Proceedings*, pages 109–124, Berne, Switzerland, 1994. Chapman & Hall.
- [149] Gerard J. Holzmann, Doron A. Peled, and Mihalis Yannakakis. On nested depth first search. In Jean-Charles Grégoire, Gerard J. Holzmann, and Doron A. Peled, editors, *Proceedings of the 2nd Spin Workshop*, volume 32 of *DIMACS: Series in Discrete Mathematics and Theoretical Computer Science*. American Mathematical Society, May 1996.
- [150] Gerard J. Holzmann. The model checker Spin. *IEEE Transactions on software Engineering*, 23(5):279–295, May 1997.
- [151] Gerard J. Holzmann. An analysis of bitstate hashing. *Formal Methods in Systems Design*, November 1998.
- [152] Jérôme Hugues, Yann Thierry-Mieg, Fabrice Kordon, Laurent Pautet, Soheib Barrir, and Thomas Vergnaud. On the formal verification of middleware behavioral properties. In *Proceedings of the 9th International Workshop on Formal Methods for Industrial Critical Systems (FMICS'04)*, volume 133 of *Electronic Notes in Theoretical Computer Science*, pages 139–157. Elsevier Science Publishers, September 2004.

- [153] `jard.89.cav` Claud Jard and Thierry Jéron. On-line model-checking for finite linear temporal logic specifications. In *Proceedings of the International Workshop on Automatic Verification Methods for Finite State Systems*, volume 407 of *LNCS*, pages 275–285, Grenoble, France, June 1989. Springer-Verlag.
- [154] `jin.02.tacas` HoonSang Jin, Kavita Ravi, and Fabio Somenzi. Fate and free will in error traces. In J.-P. Katoen and P. Stevens, editors, *Proceedings of the 8th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS'02)*, volume 2280 of *Lecture Notes in Computer Science*, pages 445–459. Springer-Verlag, 2002.
- [155] `junttila.03.phd` Tommi Junttila. *On the Symmetry Reduction Method for Petri Nets and Similar Formalisms*. PhD thesis, Helsinki University of Technology, Laboratory for Theoretical Computer Science, Espoo, Finland, 2003.
- [156] `jurdzinski.00.stacs` Marcin Jurdziński. Small progress measures for solving parity games. In *Proceedings of the 17th Symposium on Theoretical Aspects of Computer Science (STACS 2000)*, volume 1770 of *Lecture Notes in Computer Science*, pages 290–301. Springer-Verlag, February 2000.
- [157] `kesten.01.jcss` Yonit Kesten, Amir Pnueli, and Moshe Y. Vardi. Verification by augmented abstraction: The automata-theoretic view. *Journal of Computer and System Sciences*, 62(4):668–690, 2001.
- [158] `kesten.05.tcs` Yonit Kesten and Amir Pnueli. A compositional approach to CTL\* verification. *Theoretical Computer Science*, 331(2–3):397–428, February 2005.
- [159] `kesten.93.cav` Yonit Kesten, Zohar Manna, Hugh McGuire, and Amir Pnueli. A decision algorithm for full propositional temporal logic. In C. Courcoubetis, editor, *Proceedings of the 5th Conference on Computer Aided Verification (CAV'93)*, volume 697 of *Lecture Notes in Computer Science*, pages 97–109. Springer-Verlag, 1993.
- [160] `kesten.98.icalp` Yonit Kesten, Amir Pnueli, and Li on Raviv. Algorithmic verification of linear temporal logic specifications. In K.G. Larsen, S. Skyum, and G. Winskel, editors, *Proceedings of the 25th International Colloquium on Automata, Languages, and Programming (ICALP'98)*, volume 1443 of *Lecture Notes in Computer Science*, pages 1–16. Springer-Verlag, 1998.
- [161] `klai.03.phd` Kais Klai. *Réseaux de Petri: vérification symbolique et modulaire*. PhD thesis, Université Paris 6, France, 2003.
- [162] `klai.08.pn` Kais Klai and Denis Poitrenaud. MC-SOG: An LTL model checker based on symbolic observation graphs. In *Proc. of Petri Nets'08*, volume 5062 of *LNCS*, pages 288–306. Springer.
- [163] `klarlund.91.focs` Nils Klarlund. Progress measures for complementation of  $\omega$ -automata with applications to temporal logic. In *Proceedings of the 32nd annual symposium on Foundations of computer science (FOCS'91)*, pages 358–367, San Juan, Puerto Rico, 1991. IEEE Computer Society Press.
- [164] `kleene.67.ml` Stephen C. Kleene. *Mathematical Logic*. Wiley, New York, 1967.
- [165] `kupferman.86.istc` Orna Kupferman and Moshe Y. Vardi. Weak alternating automata are not that weak. In *Proceedings of the 5th Israeli Symposium on Theory of Computing and Systems (ISTC'97)*, pages 147–158. IEEE Computer Society Press, 1997.
- [166] `kupferman.98.astc` Orna Kupferman and Moshe Y. Vardi. Weak alternating automata and tree automata emptiness. In *Proceedings 30th ACM Symposium on Theory of Computing*, pages 224–233. ACM, 1998.
- [167] `kupferman.99.cav` Orna Kupferman and Moshe Y. Vardi. Model checking of safety properties. In N. Halbwachs and D. Peled, editors, *Proceedings of the 11th International Conference on Computer Aided Verification (CAV'99)*, volume 1633 of *Lecture Notes in Computer Science*, pages 172–183. Springer-Verlag, 1999.
- [168] `kurshan.98.tacas` R. Kurshan, V. Levin, M. Minea, D. Peled, and H. Yenigün. Static partial order reduction. *Tools and Algorithms for the Construction and Analysis of Systems*, pages 345–357, 1998.
- [169] `kwiatkowska.89.fac` M.Z. Kwiatkowska. Event fairness and non-interleaving concurrency. *Formal Aspects of Computing*, 1(1):213–228, 1989.
- [170] `lamport.77.ieee` L. Lamport. Proving the correctness of multiprocess programs. *IEEE Transactions on Software Engineering*, (2):125–143, 1977.
- [171] `lane.90` Sounders Mac Lane and Dirk Siefkes, editors. *The Collected Works of J. Richard Büchi*. Springer-Verlag, 1990.

- `laroussinie.02.lics` [172] François Laroussinie, Nicolas Markey, and Philippe Schnoebelen. Temporal logic with forgettable past. In *Proceedings of the 17th IEEE Symposium on Logic in Computer Science (LICS'02)*, Copenhagen, Denmark, July 2002. IEEE Computer Society Press.
- `laroussinie.95.tcs` [173] François Laroussinie and Philippe Schnoebelen. A hierarchy of temporal logics with past. *Theoretical Computer Science*, 297(1–3):297–315, 1995.
- `latvala.00.fi` [174] Timo Latvala and Keijo Heljanko. Coping with strong fairness. *Fundamenta Informaticae*, 43(1–4):1–19, 2000.
- `latvala.01.a67` [175] Timo Latvala. Model checking linear temporal logic properties of Petri nets with fairness constraints. Research Report A67, Helsinki University of Technology, Laboratory for Theoretical Computer Science, Espoo, Finland, January 2001.
- `latvala.01.icatpn` [176] Timo Latvala. Model checking LTL properties of high-level Petri nets with fairness constraints. In *Proceedings of the 22nd International Conference on Application and Theory of Petri Nets (ICATPN'01)*, volume 2075 of *Lecture Notes in Computer Science*, pages 242–262. Springer-Verlag, 2001.
- `latvala.02.a76` [177] Timo Latvala. On model checking safety properties. Technical Report A76, Helsinki University of Technology, Laboratory for Theoretical Computer Science, Espoo, Finland, 2002. Reprint of Licentiate's thesis.
- `latvala.03.spin` [178] Timo Latvala. Efficient model checking of safety properties. In T. Ball and S. K. Rajamani, editors, *Proceedings of the 10th International SPIN Workshop on Model Checking of Software*, volume 2648 of *Lecture Notes in Computer Science*, pages 74–88. Springer-Verlag, 2003.
- `leroux.03.phd` [179] Jérôme Leroux. *Algorithmique de la vérification des systèmes à compteurs. Approximation et accélération. Implémentation de l'outil FAST*. PhD thesis, École Normale Supérieure de Cachan, December 2003.
- `li.06.imsccs` [180] Yige Li, Kanglin Xie, and Tao Hao. Combining Couvreur's algorithm with bitstate-hashing for emptiness check. In *Proceedings of the First International Multi-Symposium on Computer and Computational Sciences (IMSCCS'06)*, volume 2, pages 283–286, 2006.
- `lichtenstein.85.popl` [181] Orna Lichtenstein and Amir Pnueli. Checking that finite state concurrent programs satisfy their linear specification. In *Proceedings of the 12th ACM Symposium on Principles of Programming Languages (POPL'85)*, pages 97–107. ACM, 1985.
- `lind.02.buddy` [182] Jørn Lind-Nielsen. BuDDy: Binary Decision Diagram package. Release 2.2, November 2002.
- `loding.01.ipl` [183] Christof Löding. Efficient minimization of deterministic weak  $\omega$ -automata. *Information Processing Letters*, 79(3):105–109, 2001.
- `loding.98.diplom` [184] Christof Löding. Methods for the transformation of  $\omega$ -automata: Complexity and connection to second order logic. Diploma thesis, Institut of Computer Science and Applied Mathematics, 1998.
- `loizou.82.is` [185] George Loizou and Peter Thanisch. Enumerating the cycles of a digraph: A new preprocessing strategy. *Information Sciences*, 27(3):163–182, August 1982.
- `maidl.00.focs` [186] Monika Maidl. The common fragment of CTL and LTL. In *Proceedings of the 31st Annual Symposium on Foundations of Computer Science (FOCS'00)*, pages 643–652. IEEE Computer Society, November 2000.
- `maler.06.formats` [187] Oded Maler, Dejan Nickovic, and Amir Pnueli. From mitl to timed automata. In *Proceedings of the 4th International Conference on Formal Modelling and Analysis of Timed Systems (FORMATS'06)*, volume 4202 of *Lecture Notes in Computer Science*, pages 274–289. Springer, September 2006.
- `manna.00.icalp` [188] Zohar Manna and Henry B. Sipma. Alternating the temporal picture for safety. In U. Montanari, J. D. Rolim, and E. Welzl, editors, *Proceedings of the 27th International Colloquium on Automata, Languages, and Programming (ICALP'00)*, volume 1000 of *Lecture Notes in Computer Science*, pages 429–450, Toulouse, France, July 1995. Springer-Verlag.
- `manna.90.podc` [189] Zohar Manna and Amir Pnueli. A hierarchy of temporal properties. In *Proceedings of the sixth annual ACM Symposium on Principles of distributed computing (PODC'90)*, pages 377–410, New York, NY, USA, 1990. ACM.
- `manna.95.tvrs` [190] Zohar Manna and Amir Pnueli. *Temporal Verification of Reactive Systems – Safety*. Springer-Verlag, 1995.

- `markey.02.express` [191] Nicolas Markey. Past is for free: on the complexity of verifying linear temporal properties with past. In Uwe Nestmann and Prakash Panangaden, editors, *Proceedings of the 9th International Workshop on Expressiveness in Concurrency (EXPRESS'02)*, volume 68.2 of *Electronic Notes in Theoretical of Computer Science*. Elsevier Science Publishers, 2002.
- `mazurkiewicz.87.pn` [192] A. Mazurkiewicz. Trace theory. *Petri Nets: Applications and Relationships to Other Models of Concurrency*, pages 278–324, 1987.
- `merz.01.mvpp` [193] Stephan Merz. Model checking: A tutorial overview. In F. Cassez, C. Jard, B. Rozoy, and M. Dermot, editors, *Proceedings of the 4th Summer School on Modeling and Verification of Parallel Processes (MOVEP'00)*, volume 2067 of *Lecture Notes in Computer Science*, pages 3–38. Springer-Verlag, 2001.
- `merz.03.tr` [194] Stephan Merz and Ali Sezgin. Emptiness of linear weak alternating automata. Technical report, LORIA, December 2003.
- `michel.84.stacs` [195] Max Michel. Algèbre de machines et logique temporelle. In Max Fontet and Kurt Mehlhorn, editors, *Proceedings of the Symposium on Theoretical Aspects of Computer Science (STACS'84)*, volume 166 of *Lecture Notes in Computer Science*, pages 287–298, Paris, April 1984.
- `minato.92.sasimi` [196] Shin-ichi Minato. Fast generation of irredundant sum-of-products forms from binary decision diagrams. In *Proceedings of the third Synthesis and Simulation and Meeting International Interchange workshop (SASIMI'92)*, pages 64–73, Kobe, Japan, April 1992.
- `miyano.84.tcs` [197] Satoru Miyano and Takeshi Hayashi. Alternating finite automata on  $\omega$ -words. *Theoretical Computer Science*, 32:321–330, 1984.
- `muller.84.lnsc` [198] David E. Muller and Paul E. Shupp. Alternating automata on infinite objects : Determinacy and rabin's theorem. In Maurice Nivat and Dominique Perrin, editors, *Proceedings of the École de Printemps d'Informatique Theorique on Automata on Infinite Words*, volume 192 of *Lecture Notes in Computer Science*, pages 100–107. Springer-Verlag, 1984.
- `muller.86.lnsc` [199] David E. Muller, Ahmed Saoudi, and Paul E. Shupp. Alternating automata, the weak monadic theory of the tree and its complexiy. In Laurent Kott, editor, *Proceedings 13th of the International Colloquium on Automata, Languages and Programming (ICALP'86)*, volume 226 of *Lecture Notes in Computer Science*, pages 233–244. Springer-Verlag, 1986.
- `muller.87.tcs` [200] David E. Muller and Paul. E. Shupp. Alternating automata on infinite trees. In *Theoretical Computer Science*, pages 10:267–276, 1987.
- `nalumasu.02.fm` [201] Ratan Nalumasu and Ganesh Gopalakrishnan. An efficient partial order reduction algorithm with an alternative proviso implementation. *Formal Methods in System Design*, 20(1):231–247, January 2002.
- `nalumasu.98.fmsd` [202] R. Nalumasu and G. Gopalakrishnan. A partial order reduction algorithm without the proviso. In *Formal Methods in System Design*, 1998.
- `nalumasu.98.tr` [203] Ratan Nalumasu and Ganesh Gopalakrishnan. Partial order reduction without the proviso. Technical Report UUCS-98-017, University of Utah, Department of Computer Science, 1998.
- `oddoux.03.phd` [204] Denis Oddoux. *Utilisation des automates alternants pour un model-checking efficace des logiques temporelles linéaires*. PhD thesis, Université Paris 7, Paris, France, December 2003.
- `pastor.01.tc` [205] Enric Pastor, Jordi Cortadella, and Oriol Roig. Symbolic analysis of bounded Petri nets. *IEEE Transactions on Computers*, 50(5):432–448, May 2001.
- `pastor.98.date` [206] Enric Pastor and Jordi Cortadella. Efficient encoding schemes for symbolic analysis of Petri nets. In *Proceedings of the Conference on Design, Automation and Test in Europe*, pages 790–795, Paris, March 1998.
- `pelanek.06.fimu` [207] Radek Pelánek. Web portal for benchmarking explicit model checkers. Technical report, Technical Report FIMU-RS-2006-03, Masaryk University Brno, 2006.
- `pelanek.07.spin` [208] Radek Pelánek. BEEM: benchmarks for explicit model checkers. In *Proceedings of the 14th international SPIN conference on Model checking software*, Lecture Notes in Computer Science, pages 263–267. Springer-Verlag, 2007.
- `pelanek.08.sttt` [209] Radek Pelánek. Properties of state spaces and their applications. *STTT*, 10(5):443–454, 2008.

- [210] Doron Peled, Antti Valmari, and Ilkka Kokkarinen. Relaxed visibility enhances partial order reduction. *Formal Methods in System Design*, 19(3):275–289, 2001.
- [211] D. Peled. All from one, one for all: on model checking using representatives. In *Computer Aided Verification*, Lecture Notes in Computer Science, pages 409–423. Springer, 1993.
- [212] Doron Peled. Combining partial order reductions with on-the-fly model-checking. In *Proceedings of the 6th International Conference on Computer Aided Verification (CAV'94)*, volume 818 of *Lecture Notes in Computer Science*, pages 377–390. Springer-Verlag, 1994.
- [213] Doron Peled and Wojciech Penczek. Using asynchronous Büchi automata for efficient automatic verification of concurrent systems. In *Proceedings of the 15th Workshop on Protocol Specification Testing and Verification (PSTV'95)*, pages 315–330, Warsaw, Poland, 1996. Chapman & Hall.
- [214] J.L. Peterson. Petri net theory and the modeling of systems. *PRENTICE-HALL, INC., ENGLEWOOD CLIFFS, NJ 07632, 1981, 290*, 1981.
- [215] Zohar Manna and Amir Pnueli. Specification and verification of concurrent programs by  $\forall$ -automata. In H.Barringer B.Banieqbal and A.Pnueli, editors, *Temporal Logic in Specification*, number 398 in *Lecture Notes in Computer Science*, pages 124–164. Springer-Verlag, 1987.
- [216] Irfan Pyarali, Marina Spivak, Ron Cytron, and Douglas Clark Schmidt. Evaluating and optimizing thread pool strategies for RT-CORBA. In *Proceeding of the ACM SIGPLAN workshop on Languages, compilers and tools for embedded systems (LCTES'00)*, pages 214–222. ACM, 2000.
- [217] Monika Rauch Henzinger and Jan Arne Telle. Faster algorithms for the nonemptiness of Streett automata and for communication protocol pruning. In *Proceedings of the 5th Scandinavian Workshop on Algorithm Theory (SWAT'96)*, volume 1097 of *Lecture Notes in Computer Science*, pages 16–27, Reykjavik, Iceland, July 1996. Springer-Verlag.
- [218] Kavita Ravi, Roderick Bloem, and Fabio Somenzi. A comparative study of symbolic algorithms for the computation of fair cycles. In J. W. O'Leary M. D. Aagaard, editor, *Proceedings of the 4th International Conference on Formal Methods in Computer Aided Design (FMCAD'00)*, volume 2517 of *Lecture Notes in Computer Science*, pages 143–160. Springer-Verlag, 2000.
- [219] Mauno Rönkkö. LBT: LTL to Büchi conversion. <http://www.tcs.hut.fi/Software/maria/tools/lbt/>, 1999. Implements [121].
- [220] Kristin Y. Rozier and Moshe Y. Vardi. LTL satisfiability checking. In *Proceedings of the 12th International SPIN Workshop on Model Checking of Software (SPIN'07)*, volume 4595 of *Lecture Notes in Computer Science*, pages 149–167. Springer, 2007.
- [221] Shmuel Safra. *Complexity of Automata on Infinite Objects*. PhD thesis, The Weizmann Institute of Science, Rehovot, Israel, March 1989.
- [222] Shmuel Safra and Moshe Y. Vardi. On  $\omega$ -automata and temporal logic. In *Proceedings of the twenty-first annual ACM Symposium on Theory of Computing (STOC'89)*, pages 127–137. ACM, 1989.
- [223] Shmuel Safra. Exponential determinization for  $\omega$ -automata with strong-fairness acceptance condition. In *Proceedings of the 24th ACM Symposium on Theory of Computing*. ACM, May 1992.
- [224] Klaus Schneider. Improving automata generation for linear temporal logic by considering the automaton hierarchy. In *Proceedings of the 8th International Conference on Logic for Programming, Artificial Intelligence and Reasoning*, volume 2250 of *Lecture Notes in Artificial Intelligence*, pages 39–54, Havana, Cuba, 2001. Springer-Verlag.
- [225] Klaus Schneider. CTL and equivalent sublanguages of CTL\*. In C. Delgado Kloos, editor, *Proceedings of International Conference on Computer Hardware Description Languages and their Applications (CHDL'97)*, pages 40–59, Toledo, Spain, 1997. Chapman and Hall.
- [226] Claus Schröter, Stefan Schwoon, and Javier Esparza. The Model-Checking Kit. In Wil van der Aalst and Eike Best, editors, *Proceedings of the 24th International Conference on Applications and Theory of Petri Nets (ATPN'03)*, volume 2679 of *Lecture Notes in Computer Science*, pages 463–472. Springer-Verlag, June 2003.

- `schwendimann.98.tableaux` [227] Stefan Schwendimann. A new one-pass tableau calculus for PLTL. In H. de Swart, editor, *Automated Reasoning with Analytic Tableaux and Related Methods, proceedings of TABLEAUX'98*, volume 1397 of *Lecture Notes in Computer Science*, pages 277–291. Springer-Verlag, 1998.
- `schwoon.04.tr` [228] Stefan Schwoon and Javier Esparza. A note on on-the-fly verification algorithms. Technical report, Universität Stuttgart, Fakultät Informatik, Elektrotechnik und Informationstechnik, November 2004.
- `schwoon.05.tacas` [229] Stefan Schwoon and Javier Esparza. A note on on-the-fly verification algorithms. In Nicolas Halbwachs and Lenore Zuck, editors, *Proceedings of the 11th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS'05)*, volume 3440 of *Lecture Notes in Computer Science*, Edinburgh, Scotland, April 2005. Springer.
- `sebastiani.03.charme` [230] Roberto Sebastiani and Stefano Tonetta. "more deterministic" vs. "smaller" Büchi automata for efficient LTL model checking. In G. Goos, J. Hartmanis, and J. van Leeuwen, editors, *Proceedings of the 12th Advanced Research Working Conference on Correct Hardware Design and Verification Methods (CHARME'03)*, volume 2860 of *Lecture Notes in Computer Science*, pages 126–140, L'Aquila, Italy, October 2003. Springer-Verlag.
- `sebastiani.03.tr` [231] Roberto Sebastiani and Stefano Tonetta. "more deterministic" vs. "smaller" Büchi automata for efficient LTL model checking. Technical Report #DIT-03-041, University of Trento, July 2003. Extended version of [230].
- `sebastiani.05.cav` [232] Roberto Sebastiani, Stefano Tonetta, and Moshe Y. Vardi. Symbolic systems, explicit properties: on hybrid approaches for LTL symbolic model checking. In Kousha Etessami and Sri-ram K. Rajamani, editors, *Proceedings of 17th International Conference on Computer Aided Verification (CAV'05)*, volume 3576 of *Lecture Notes in Computer Science*, pages 350–363, Edinburgh, Scotland, UK, July 2005. Springer.
- `smullyan.68.fol` [233] Raymon M. Smullyan. *First-Order Logic*. Springer-Verlag, 1968.
- `somenzi.00.cav` [234] Fabio Somenzi and Roderick Bloem. Efficient Büchi automata for LTL formulæ. In *Proceedings of the 12th International Conference on Computer Aided Verification (CAV'00)*, volume 1855 of *Lecture Notes in Computer Science*, pages 247–263, Chicago, Illinois, USA, 2000. Springer-Verlag.
- `spot.2011` [235] MoVe/LRDE. The Spot home page: <http://spot.lip6.fr>, 2011.
- `stern.95.charme` [236] Ulrich Stern and David L. Dill. Improved probabilistic verification by hash compaction. In P.E. Camurati and H. Eveking, editors, *Proceedings of the Advanced Research Working Conference on Correct Hardware Design and Verification Methods (CHARME'95)*, volume 987 of *Lecture Notes in Computer Science*, pages 206–224. Springer-Verlag, 1995.
- `tabakov.07.lata` [237] Deian Tabakov and Mushe Y. Vardi. Model checking büchi specifications. In *Proceedings of the 1st International Conference on Language and Automata Theory and Applications (LATA'07)*, *Lecture Notes in Computer Science*. Springer, April 2007. To appear.
- `tabakov.10.rv` [238] Deian Tabakov and Moshe Y. Vardi. Optimized temporal monitors for SystemC. In *Proceedings of the 1st International Conference on Runtime Verification (RV'10)*, volume 6418 of *Lecture Notes in Computer Science*, pages 436–451. Springer, November 2010.
- `tarjan.71.ssat` [239] Robert Tarjan. Depth-first search and linear graph algorithms. In *Conference records of the 12th Annual IEEE Symposium on Switching and Automata Theory*, pages 114–121. IEEE, October 1971. Later republished as [240].
- `tarjan.72.siam` [240] Robert Tarjan. Depth-first search and linear graph algorithms. *SIAM Journal on Computing*, 1(2):146–160, 1972.
- `tauriainen.00.a66` [241] Heikki Tauriainen. Automated testing of Büchi automata translators for Linear Temporal Logic. Research Report A66, Helsinki University of Technology, Laboratory for Theoretical Computer Science, Espoo, Finland, 2000. Reprint of Master's thesis.
- `tauriainen.00.spin` [242] Heikki Tauriainen and Keijo Heljanko. Testing SPIN's LTL formula conversion into Büchi automata with randomly generated input. In K. Havelund, J. Penix, and W. Visser, editors, *Proceedings of the 7th International SPIN Workshop on Model Checking of Software (SPIN'2000)*, volume 1885 of *Lecture Notes in Computer Science*, pages 54–72, Stanford University, California, USA, August 2000. Springer-Verlag.
- `tauriainen.02.sttt` [243] Heikki Tauriainen and Keijo Heljanko. Testing LTL formula translation into Büchi automata. *International Journal on Software Tools for Technology Transfer*, 4(1):57–70, 2002.

- `tauriainen.03.a79` [244] Heikki Tauriainen. Nested emptiness search for generalized Büchi automata. Research Report A79, Helsinki University of Technology, Laboratory for Theoretical Computer Science, Espoo, Finland, July 2003.
- `tauriainen.03.a83` [245] Heikki Tauriainen. On translating linear temporal logic into alternating and nondeterministic automata. Research Report A83, Helsinki University of Technology, Laboratory for Theoretical Computer Science, Espoo, Finland, December 2003. Reprint of Licentiate's thesis.
- `tauriainen.04.acsd` [246] Heikki Tauriainen. Nested emptiness search for generalized Büchi automata. In *Proceedings of the 4th International Conference on Application of Concurrency to System Design (ACSD'04)*, pages 165–174. IEEE Computer Society, June 2004.
- `tauriainen.05.a96` [247] Heikki Tauriainen. A note on the worst-case memory requirements of generalized nested depth-first search. Research Report A96, Helsinki University of Technology, Laboratory for Theoretical Computer Science, Espoo, Finland, September 2005.
- `tauriainen.06.phd` [248] Heikki Tauriainen. *Automata and Linear Temporal Logic: Translation with Transition-based Acceptance*. PhD thesis, Helsinki University of Technology, Espoo, Finland, September 2006.
- `tauriainen.99` [249] Heikki Tauriainen. A randomized testbench for algorithms translating linear temporal logic formulæ into Büchi automata. In H-D. Burkhard, L. Czaja, H-S. Nguyen, and P. Starke, editors, *Proceedings of the Concurrency, Specification and Programming 1999 Workshop (CS&P'99)*, pages 251–262, Warsaw, Poland, September 1999.
- `thierry-mieg.03.forte` [250] Yann Thierry-Mieg, Jean-Michel Ilié, and Denis Poitrenaud. A symbolic symbolic state space representation. In *Proceedings of the 24th IFIP WG 6.1 International Conference on Formal Techniques for Networked and Distributed Systems (FORTE'04)*, Madrid, Spain, September 2004.
- `thierry-mieg.03.icatpn` [251] Yann Thierry-Mieg, Claude Dutheillet, and Isabelle Mounier. Automatic symmetry detection in well-formed nets. In Wil van der Aalst and Eike Best, editors, *Proceedings of the 24th International Conference on Application and Theory of Petri Nets (ICATPN'03)*, volume 2679 of *Lecture Notes in Computer Science*, pages 82–101, Eindhoven, The Netherlands, June 2003. Springer-Verlag.
- `thierry-mieg.04.gl` [252] Yann Thierry-Mieg, Soheib Baarir, Alexandre Duret-Lutz, and Fabrice Kordon. Nouvelles techniques de model-checking pour la vérification de systèmes complexes. *Génie Logiciel*, (69):17–23, June 2004.
- `thierry-mieg.04.phd` [253] Yann Thierry-Mieg. *Technique pour le Model-Checking de spécifications de Haut Niveau*. PhD thesis, Université de Paris 6, LIP6, Paris, France, December 2004. In English.
- `thirioux.02.fmics` [254] Xavier Thirioux. Simple and efficient translation from LTL formulas to Büchi automata. In Rance Cleaveland and Hubert Garavel, editors, *Proceedings of the 7th International ERCIM Workshop in Formal Methods for Industrial Critical Systems (FMICS'02)*, volume 66(2) of *Electronic Notes in Theoretical Computer Science*, Málaga, Spain, July 2002. Elsevier.
- `thomas.96.tr` [255] Wolfgang Thomas. Languages, automata, and logic. Technical Report 9607, Institut für Informatik und Praktische Mathematik, Christian-Albrechts-Universität zu Kiel, Germany, May 1996.
- `thomas.99.jewels` [256] Wolfgang Thomas. Complementation of Büchi automata revisited. In J. Karhumäki, H.A. Maurer, G. Paun, and G. Rozenberg, editors, *Jewels are Forever, Contributions on Theoretical Computer Science in Honor of Arto Salomaa*, pages 109–120. Springer-Verlag, 1999.
- `tretmans.92.phd` [257] Jan Tretmans. *A Formal Approach to Conformance Testing*. PhD thesis, University of Twente, Enschede, The Netherlands, 1992.
- `tretmans.99.concur` [258] Jan Tretmans. Testing concurrent systems: A formal approach. In *Proceedings of the 10th International Conference on Concurrency Theory (CONCUR'99)*, volume 1664 of *Lecture Notes in Computer Science*, pages 46–65. Springer-Verlag, 1999.
- `tsay.11.tacas` [259] Yih-Kuen Tsay, Ming-Hsien Tsai, Jinn-Shu Chang, and Yi-Wen Chang. Büchi store: An open repository of büchi automata. In *Proceedings of the 17th International Conference Tools and Algorithms for the Construction and Analysis of Systems (TACAS'11)*, volume 6605 of *Lecture Notes in Computer Science*, pages 262–266. Springer, March 2011.
- `valmari.88.icatpn` [260] A. Valmari. Error detection by reduced reachability graph generation. In *Proc. 9th International Conference on Application and Theory of Petri Nets*, pages 95–112, 1988.

- `valmari.91.icatpn` [261] Antti Valmari. Stubborn sets for reduced state space generation. In *Proceedings of the 10th International Conference on Applications and Theory of Petri Nets (ICATPN'91)*, volume 618 of *Lecture Notes in Computer Science*, pages 491–515, London, UK, 1991. Springer-Verlag.
- `valmari.93.cav` [262] Antti Valmari. On-the-fly verification with stubborn sets. In *Proceedings of the 5th International Conference on Computer Aided Verification (CAV '93)*, pages 397–408, London, UK, 1993. Springer-Verlag.
- `valmari.98.lpn` [263] Antti Valmari. The state explosion problem. In W. Reisig and G. Rozenberg, editors, *Lectures on Petri Nets 1: Basic Models*, volume 1491 of *Lecture Notes in Computer Science*, pages 429–528. Springer-Verlag, 1998.
- `vardi.01.tacas` [264] Moshe Y. Vardi. Branching vs. linear time: Final showdown. In T. Margaria and W. Yi, editors, *Proceedings of the 7th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS'01)*, volume 2031 of *Lecture Notes in Computer Science*, pages 1–22, Genova, Italy, April 2001. Springer-Verlag.
- `vardi.05.alc` [265] Moshe Y. Vardi. Büchi complementation: A forty-year saga. In *5th symposium on Atomic Level Characterizations (ALC'05)*, 2005. Invited extended abstract.
- `vardi.07.stacs` [266] Moshe Y. Vardi. The Büchi complementation saga. In *Proceedings of the 17th Symposium on Theoretical Aspects of Computer Science (STACS'07)*, Aachen, Germany, February 2007. Invited paper.
- `vardi.07.vmcai` [267] Moshe Y. Vardi. Automata-theoretic model checking revisited. In *Proceedings of the 8th International Conference on Verification, Model Checking and Abstract Interpretation (VMCAI'07)*, volume 4349 of *Lecture Notes in Computer Science*, Nice, France, January 2007. Springer. Invited paper.
- `vardi.86.lics` [268] Moshe Y. Vardi. An automata-theoretic approach to automatic program verification. In *Proceedings of the 1st IEEE Symposium on Logic in Computer Science (LICS'86)*, pages 332–344. IEEE Computer Society Press, 1986.
- `vardi.94.ic` [269] Moshe Y. Vardi and Pierre Wolper. Reasoning about infinite computations. *Information and Computation*, 115(1):1–37, November 1994.
- `vardi.95.cst` [270] Moshe Y. Vardi. Alternating automata and program verification. In J. van Leeuwen, editor, *Computer Science Today, Recent Trends and Developments*, volume 1000 of *Lecture Notes in Computer Science*, pages 471–485. Springer-Verlag, 1995.
- `vardi.96.banff` [271] Moshe Y. Vardi. An automata-theoretic approach to linear temporal logic. In Faron Moller and Graham M. Birtwistle, editors, *Proceedings of the 8th Banff Higher Order Workshop (Banff'94)*, volume 1043 of *Lecture Notes in Computer Science*, pages 238–266, Banff, Alberta, Canada, 1996. Springer-Verlag.
- `varpaaniemi.97.cav` [272] Kimmo Varpaaniemi, Keijo Heljanko, and Johan Lilius. PROD 3.2: An advanced tool for efficient reachability analysis. In *Proceedings of the 9th International Conference on Computer Aided Verification (CAV'97)*, volume 1254 of *Lecture Notes in Computer Science*, pages 472–475. Springer-Verlag, 1997.
- `varpaaniemi.98.icatpn` [273] Kimmo Varpaaniemi. On stubborn sets in the verification of linear time temporal properties. In J. Desel and M. Silva, editors, *Proceedings of the 19th International Conference on Application and Theory of Petri Nets (ICATPN'98)*, volume 1420 of *Lecture Notes in Computer Science*, pages 124–143. Springer-Verlag, 1998.
- `varpaaniemi.98.phd` [274] Kimmo Varpaaniemi. *On The Stubborn Set Method in Reduced State Space Generation*. PhD thesis, Helsinki University of Technology, May 1998.
- `vernadat.96.icatpn` [275] Francois Vernadat, Pierre Azema, and Francois Michel. Covering step graph. In *Proceedings of the 17th International Conference on Application and Theory of Petri Nets (ICATPN'96)*, volume 1091 of *Lecture Notes in Computer Science*, pages 516–535. Springer-Verlag, 1996.
- `visser.98.phd` [276] Willem C. Visser. *Efficient CTL\* Model Checking Using Games and Automata*. PhD thesis, University of Manchester, June 1998.
- `vries.98.spin` [277] René G. de Vries and Jan Tretmans. On-the-fly conformance testing using Spin. In *Fourth Workshop on Automata Theoretic Verification with the Spin Model Checker (SPIN'98)*, ENST 98 S 002, pages 115–128, Paris, France, November 1998. Ecole Nationale Supérieure des Télécommunications.



- wang.01.concur [278] Chao Wang, Roderick Bloem, Gary D. Hachtel, Kavita Ravi, and Fabio Somenzi. Divide and compose: SCC refinement for language emptiness. In K. G. Larsen and M. Nielsen, editors, *Proceedings of the 12th International Conference on Concurrency Theory (Concur'01)*, volume 2154 of *Lecture Notes in Computer Science*, pages 456–471. Springer-Verlag, 2001.
- wang.02.fmcad [279] Chao Wang and Gary D. Hachtel. Sharp disjunctive decomposition for language emptiness checking. In *Proceedings of the 4th symposium on Formal Methods in Computer Aided Design*, volume 2517 of *Lecture Notes in Computer Science*, pages 105–122. Springer-Verlag, 2002.
- wimmer.06.atva [280] Ralf Wimmer, Marc Herbstritt, Holger Hermanns, Kelley Strampp, and Bernd Becker. Sigref — a symbolic bisimulation tool box. In Susanne Graf and Wenhui Zhang, editors, *Proceedings of the 4th International Symposium on Automated Technology for Verification and Analysis (ATVA'06)*, volume 4218 of *Lecture Notes in Computer Science*, pages 477–492. Springer-Verlag, October 2006.
- wolfgang.02.dlt [281] Wolfgang Thomas. A short introduction to infinite automata. In Werner Kuich, Grzegorz Rozenberg, and Arto Salomaa, editors, *Proceedings of the 5th International Conference on Developments in Language Theory (DLT'01)*, volume 2295 of *Lecture Notes in Computer Science*, pages 130–144. Springer-Verlag, 2002.
- wolper.00.fmpa [282] Pierre Wolper. Constructing automata from temporal logic formulas: A tutorial. In E. Brinksma, H. Hermanns, and J.-P. Katoen, editors, *Proceedings of the FMPA 2000 summer school*, volume 2090 of *Lecture Notes in Computer Science*, pages 261–277, Nijmegen, the Netherlands, July 2000. Springer-Verlag.
- wolper.83.focs [283] Pierre Wolper, Moshe Y. Vardi, and Aravinda Prasad Sistla. Reasoning about infinite computation paths. In *Proceedings of the 24th IEEE Symposium on Foundations of Computer Science (FOCS'83)*, pages 185–194. IEEE Computer Society Press, 1983. Later extended and published as [269].
- wolper.83.ic [284] Pierre Wolper. Temporal logic can be more expressive. *Information and Control*, 56(1–2):72–99, 1983.
- wolper.85.la [285] Pierre Wolper. The tableau method for temporal logic: An overview. *Logique et Analyse*, (110–111):119–136, 1985.
- wolper.93.cav [286] Pierre Wolper and Denis Leroy. Reliable hashing without collision detection. In C. Courcoubetis, editor, *Proceedings of the 5th Conference on Computer Aided Verification (CAV'93)*, volume 697 of *Lecture Notes in Computer Science*, pages 59–70. Springer-Verlag, 1993.